







# **LKS 安全型控制系统**

## **指令手册**

**V1.0**

**2019 06**





r4)upP=















# 1

## 1.1

V1.0	2017.08.30	
V1.1	2018.02.02	
V1.2	2018.11.31	<a href="#">2.3.2</a>
V1.0	2019.06.13	

## 1.2

## 1.3

LKS

## 1.4

### 1.4.1

### 1.4.2

### 1.4.3

Enter

### 1.4.4

F1

### 1.4.5



+



Ctrl+I

POU

1



## 1.5



Safety FA-AutoThink



LKS



LKS



## 2

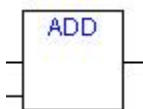
### 2.1

sysGetDPConfirmState	HS_DPDIAG.HLF
sysGetDPSlaveComState	
DIProc	HS_IOPROC.HLF

### 2.2

#### 2.2.1

##### 2.2.1.1 ADD



1

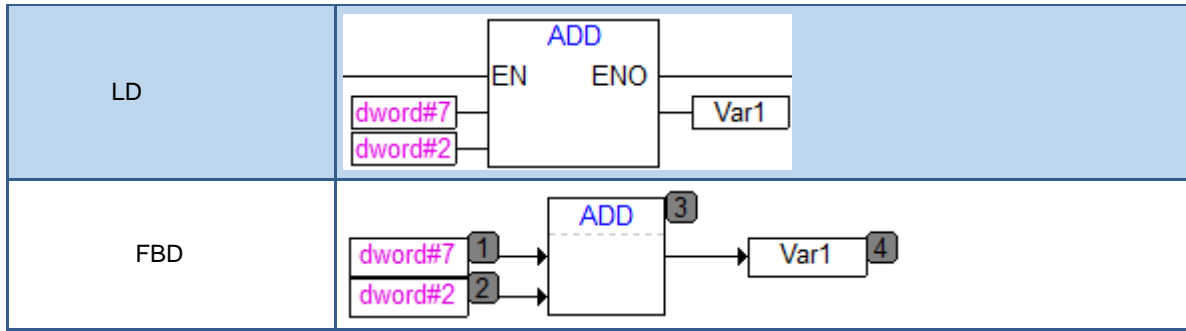
1.

		DWORD	DINT	REAL	LREAL	TIME
		DWORD	DINT	REAL	LREAL	TIME

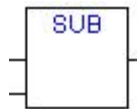
2.

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		DWORD	0	G区



### 2.2.1.2 SUB



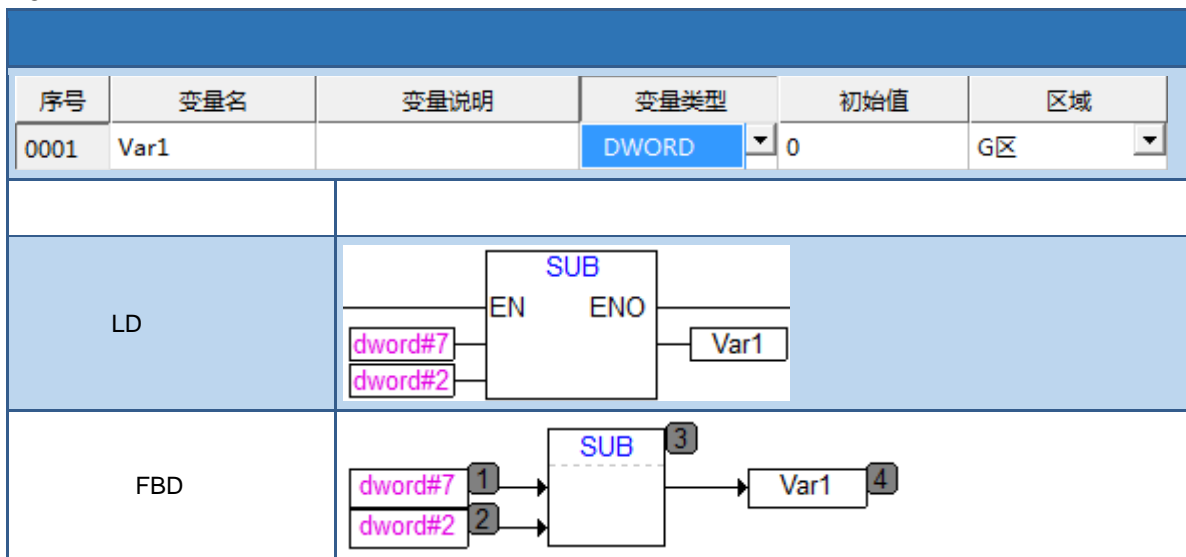
2

1.

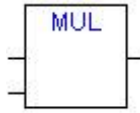
		DWORD	DINT	REAL	LREAL	TIME
		DWORD	DINT	REAL	LREAL	TIME

2.

3.



### 2.2.1.3 MUL



3

1.

		DWORD	DINT	REAL LREAL
		DWORD	DINT	REAL LREAL

2.

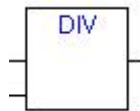
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		DWORD	0	G区

LD	
FBD	

### 2.2.1.4 DIV



4

1.

		DWORD	DINT	REAL LREAL

		DWORD DINT REAL LREAL
--	--	-----------------------

2.

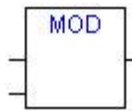
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		DWORD	0	G区

LD	
FBD	

2.2.1.5 MOD



5

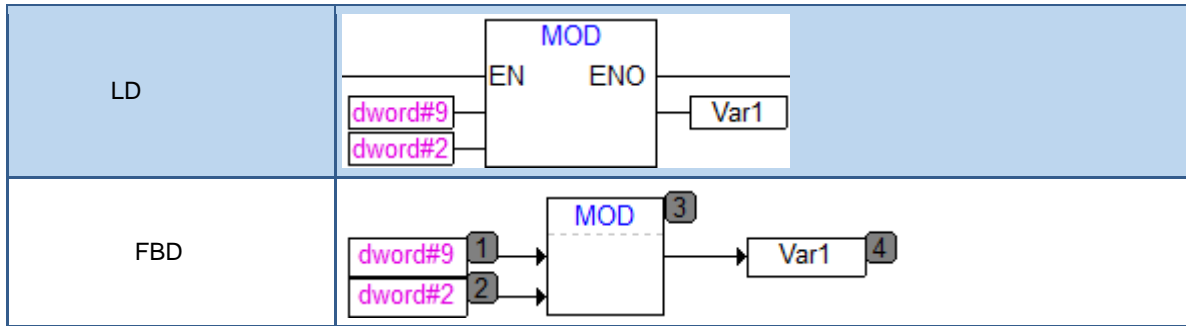
1.

		DWORD DINT
		DWORD DINT

2.

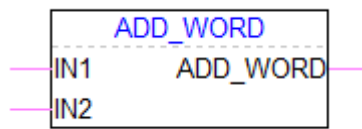
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		DWORD	0	G区



**2.2.1.6 ADD\_WORD**

WORD



6

1.

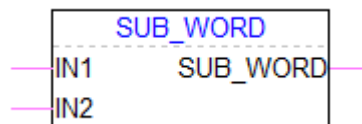
IN1	WORD
IN2	WORD
ADD_WORD	WORD

2.

[2.2.1.1 ADD](#)

**2.2.1.7 SUB\_WORD**

WORD



7

1.

--	--

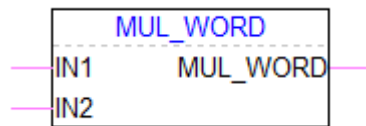
IN1	WORD
IN2	WORD
SUB_WORD	WORD

2.

## 2.2.1.2 SUB

## 2.2.1.8 MUL\_WORD

WORD



8

1.

IN1	WORD
IN2	WORD
MUL_WORD	WORD

2.

## 2.2.1.3 MUL

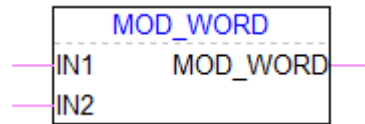
IN1	WORD
IN2	WORD
DIV_WORD	WORD

2.

2.2.1.4 DIV

2.2.1.10 MOD\_WORD

WORD



10

1.

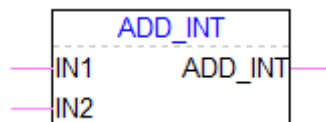
IN1	WORD
IN2	WORD
MOD_WORD	WORD

2.

2.2.1.5 MOD

2.2.1.11 ADD\_INT

INT



11

1.

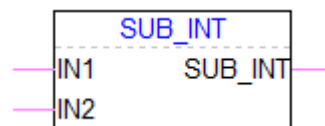
IN1	INT
IN2	INT
ADD_INT	INT

2.

2.2.1.1 ADD

## 2.2.1.12 SUB\_INT

INT



12

1.

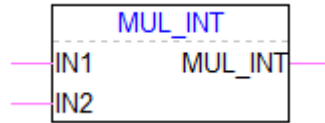
IN1	INT
IN2	INT
SUB_INT	INT

2.

2.2.1.2 SUB

## 2.2.1.13 MUL\_INT

INT



13

1.

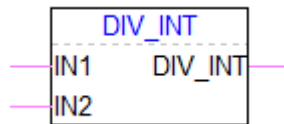
IN1	INT
IN2	INT
MUL_INT	INT

2.

2.2.1.3 MUL

2.2.1.14 DIV\_INT

INT



14

1.

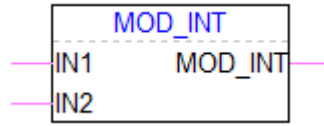
IN1	INT
IN2	INT
DIV_INT	INT

2.

2.2.1.4 DIV

### 2.2.1.15 MOD\_INT

INT



15

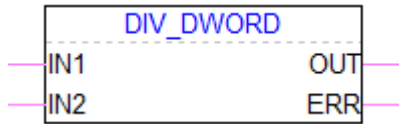
1.

IN1	INT
IN2	INT
MOD_INT	INT

2.

#### 2.2.1.5 MOD

### 2.2.1.16 DIV\_DWORD



16

1.

IN1	DWORD		0	
IN2	DWORD		0	0
OUT	DWORD		0	



ERR	BOOL	ERR=0		
		ERR=1	0	0
		OUT		

2.

IN2 0

IN2 0

ERR

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	DIV_DINT_1		DIV_DINT		G区
0002					
0003					

DIV\_DINT\_1

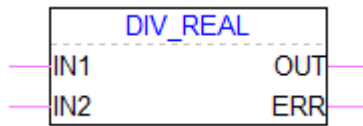
DIV\_DINT

	EN	ENO	
dint#8	IN1	OUT	Var1
dint#2	IN2	ERR	Var2

FBD

### 2.2.1.18 DIV\_REAL



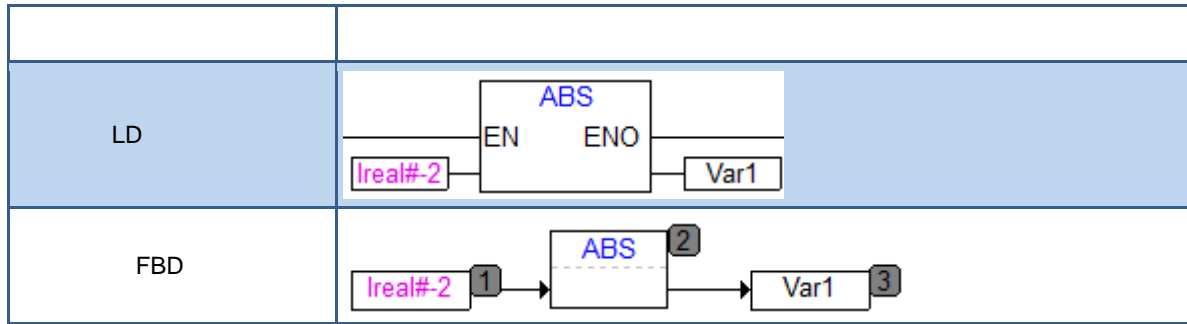
18

1.

IN1	REAL		0	
IN2	REAL		0	0
OUT	REAL		0	
ERR	BOOL	ERR=0	0	





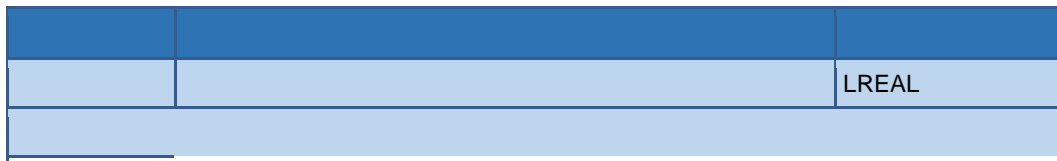


2.2.1.21 SQRT



21

1.



LREAL

1.

		LREAL
		LREAL

2.

10

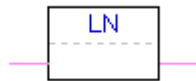
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		LREAL	0	G区

LD	
FBD	

2.2.1.23 LN



23

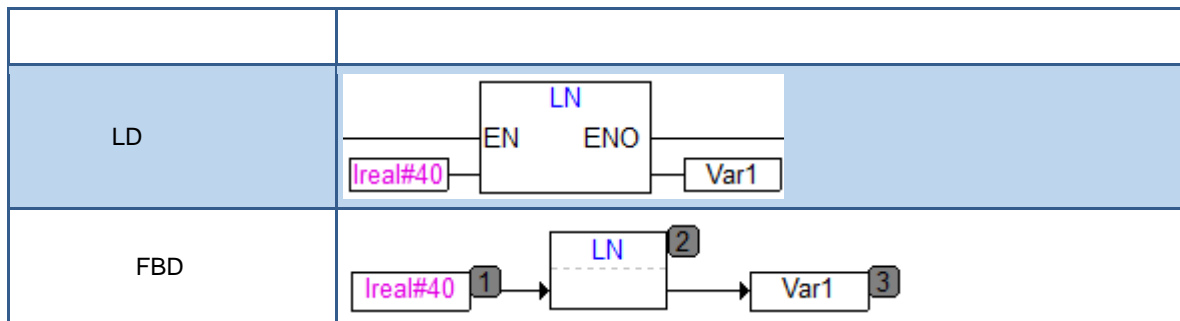
1.

		LREAL
		LREAL

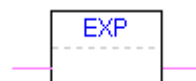
2.

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		LREAL	0	G区



### 2.2.1.24 EXP



24

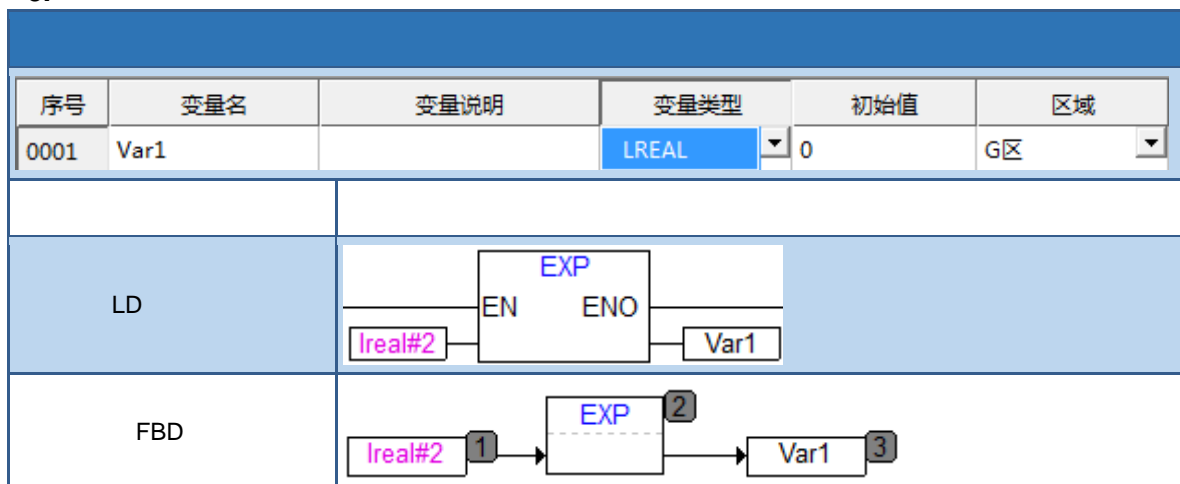
1.

		LREAL
		LREAL

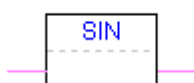
2.

e

3.



### 2.2.1.25 SIN



25

1.

		LREAL	LREAL	64 bits	52 bits
		LREAL			

2.

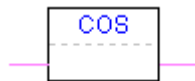
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		LREAL	0	G区

LD	
FBD	

2.2.1.26 COS



26

1.

		LREAL	LREAL	64 bits	52 bits
		LREAL			

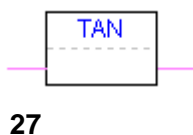
2.

3.



序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		LREAL	0	G区
LD					
FBD					

2.2.1.27 TAN



1.

		LREAL	LREAL	64 bits	52 bits
		LREAL			

2.

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		LREAL	0	G区
LD					
FBD					

### 2.2.1.28 ASIN



28

1.

		LREAL	
		LREAL	

2.

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		LREAL	0	G区
LD	<p>Ladder logic diagram showing an LD instruction connected to an ASIN function block. The EN input is connected to 'lreal#0.5' and the ENO output is connected to 'Var1'.</p>				
FBD	<p>Function block diagram showing an ASIN block with input 'lreal#0.5' (labeled 1), output 'Var1' (labeled 3), and a block number '2'.</p>				

### 2.2.1.29 ACOS



29

1.

		LREAL	
		LREAL	

2.

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		LREAL	0	G区

LD	
FBD	

2.2.1.30 ATAN



30

1.

序号	变量名	变量说明	变量类型	初始值	区域
			LREAL		
			LREAL		

2.

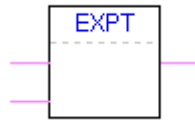
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		LREAL	0	G区

LD	
FBD	

## 2.2.1.31 EXPT



31

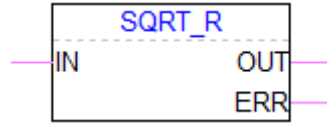
1.

1	1 Y	LREAL
2	2 n	LREAL
		LREAL

2.1

1

### 2.2.1.32 SQRT\_R



32

1.

IN	REAL		0	
OUT	REAL		0	
ERR	BOOL	ERR=0 ERR=1 OUT	IN	0

2.

IN >= 0

IN < 0 ERR

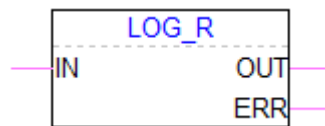
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	SQRT_R_1		SQRT_R		G区
0002	Var1		REAL	0	G区
0003	Var2		BOOL	FALSE	G区

LD	<p>The diagram shows a normally closed contact labeled 'LD'. The contact is connected to the 'EN' port of a function block labeled 'SQRT_R_1'. The 'SQRT_R_1' block has an input 'IN' connected to a variable 'real#9'. The 'SQRT_R_1' block has two outputs: 'OUT' connected to 'Var1' and 'ERR' connected to 'Var2'.</p>
FBD	<p>The diagram shows the 'SQRT_R_1' function block in a Function Block Diagram (FBD) style. The input 'IN' is connected to 'real#9' and is labeled with a grey box containing the number '1'. The output 'OUT' is connected to 'Var1' and is labeled with a grey box containing the number '3'. The output 'ERR' is connected to 'Var2' and is labeled with a grey box containing the number '4'. The function block itself is labeled 'SQRT_R' and is labeled with a grey box containing the number '2'.</p>

### 2.2.1.33 LOG\_R



33 10

1.

IN	REAL		0	
OUT	REAL		0	
ERR	BOOL	ERR=0 ERR=1	OUT	0

2.

IN>0                      10                      IN<=0    ERR

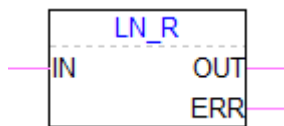
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	LOG_R_1		LOG_R		G区
0002	Var1		REAL	0	G区
0003	Var2		BOOL	FALSE	G区

LD	
FBD	

### 2.2.1.34 LN\_R



34

1.

IN	REAL		0	
----	------	--	---	--

OUT	REAL		0	
ERR	BOOL	ERR=0 ERR=1 OUT	0	

2.

IN>0

IN<=0 ERR

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	LN_R_1		LN_R		G区
0002	Var1		REAL	0	G区
0003	Var2		BOOL	FALSE	G区

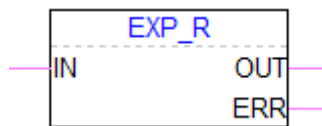
  

LD

FBD

### 2.2.1.35 EXP\_R



35

1.

IN	REAL		0	88
OUT	REAL		0	
ERR	BOOL	ERR=0 ERR=1 OUT 88	0	

2.

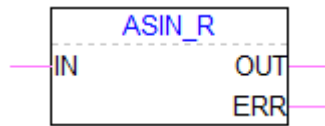
IN<=88 e

IN>88 ERR

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	EXP_R_1		EXP_R		G区
0002	Var1		REAL		G区
LD					
FBD					

2.2.1.36 ASIN\_R



36

1.

IN	REAL		0	[-1,1]
OUT	REAL		0	
ERR	BOOL	ERR=0 ERR=1 [-1,1] OUT	0	

2.

-1<=IN<=1

IN>1 IN<-1 ERR



序号	变量名	变量说明	变量类型	初始值	区域
0001	ACOS_R_1		ACOS_R		G区
0002	Var1		REAL	0	G区
0003	Var2		BOOL	FALSE	G区

LD	
FBD	

### 2.2.1.38 EXPT\_R



38

1.

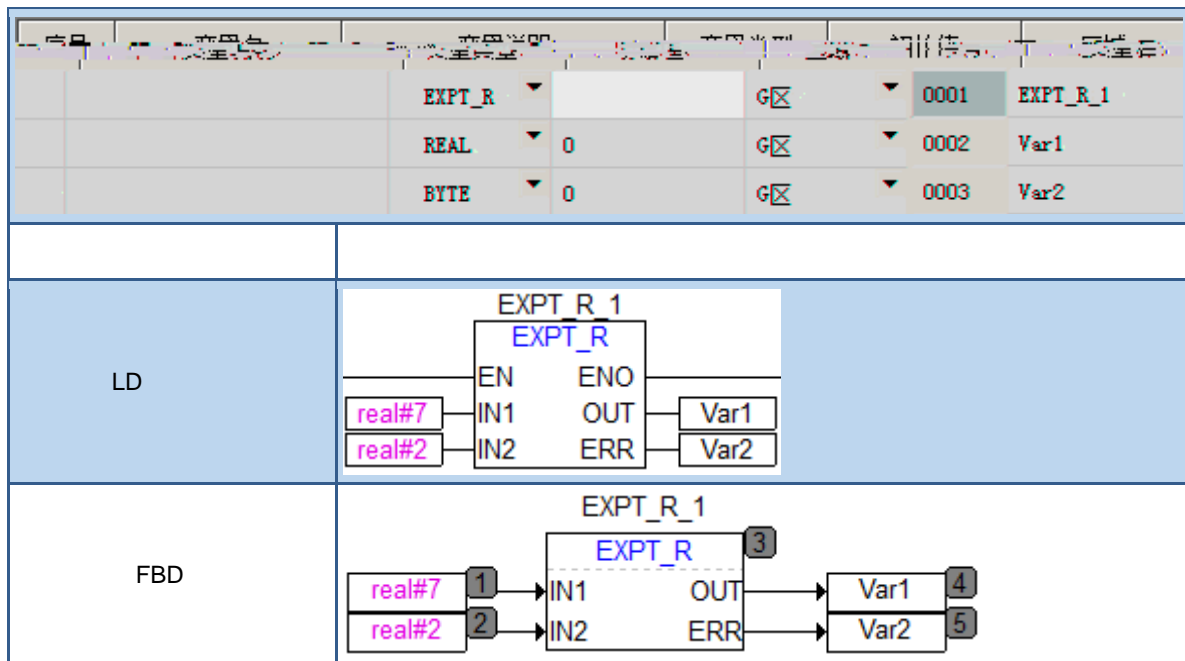
IN1	REAL	1	0	[0, +
IN2	REAL	2	0	
OUT	REAL		0	
ERR	BYTE	ERR=0 ERR=1 ERR=2 ERR=3 OUT	IN1 IN1=0 IN2<=0 ln ln IN1   +ln ln2 >4.466	OUT 0

2.

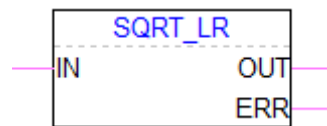
2      1      2      ERR      0      1      2      1

3.





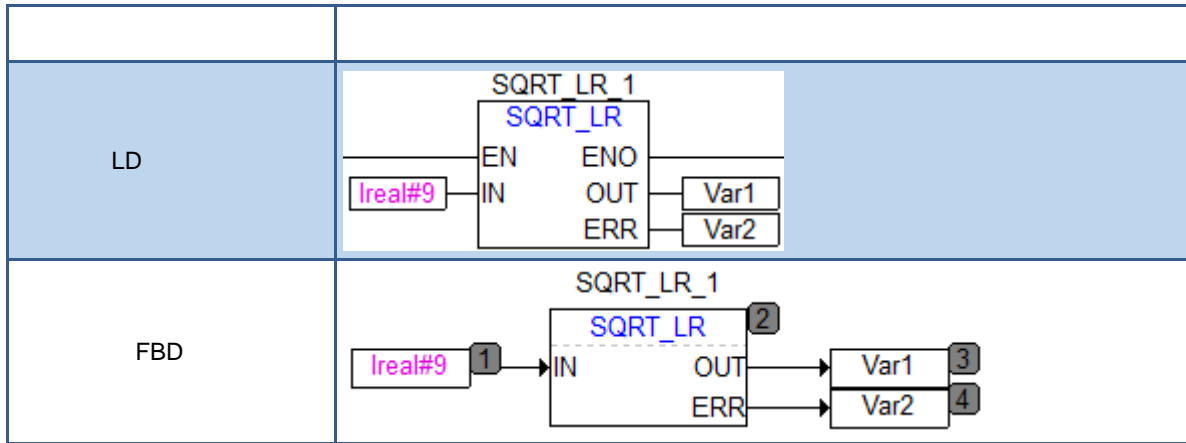
### 2.2.1.39 SQRT\_LR



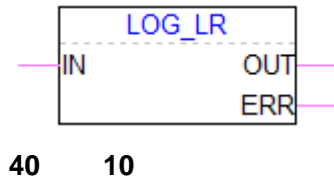
39

1.



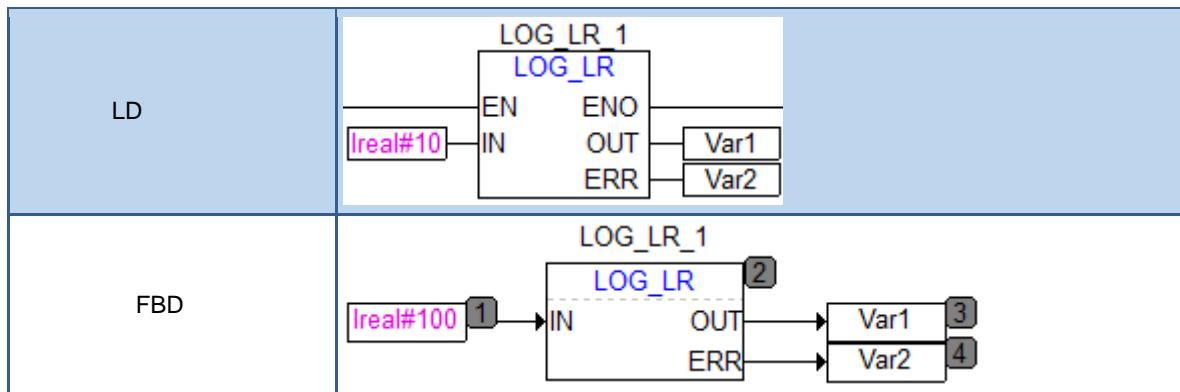


2.2.1.40 LOG\_LR

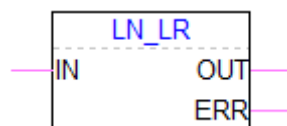


1.





### 2.2.1.41 LN\_LR



41

1.

Port	Type	Description	Initial Value	Area
IN	LREAL		0	
OUT	LREAL		0	
ERR	BOOL	ERR=0 ERR=1 OUT	0	

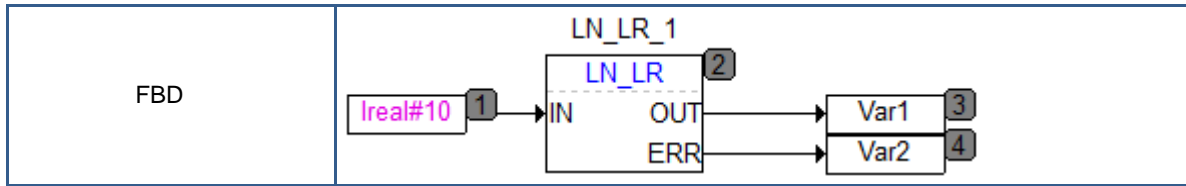
2.

IN>0

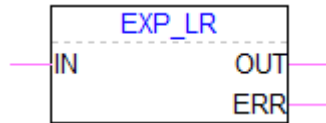
IN<=0 ERR

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	LN LR 1		LN_LR		G区



2.2.1.42 EXP\_LR

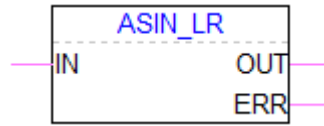


42

1.

IN	LREAL		0	709
OUT	LREAL		0	

### 2.2.1.43 ASIN\_LR

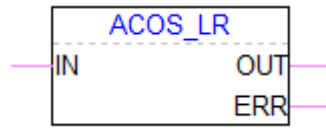


43

1.



### 2.2.1.44 ACOS\_LR



44

1.

IN	LREAL		0	[-1,1]
----	-------	--	---	--------

### 2.2.1.45 EXPT\_LR



45

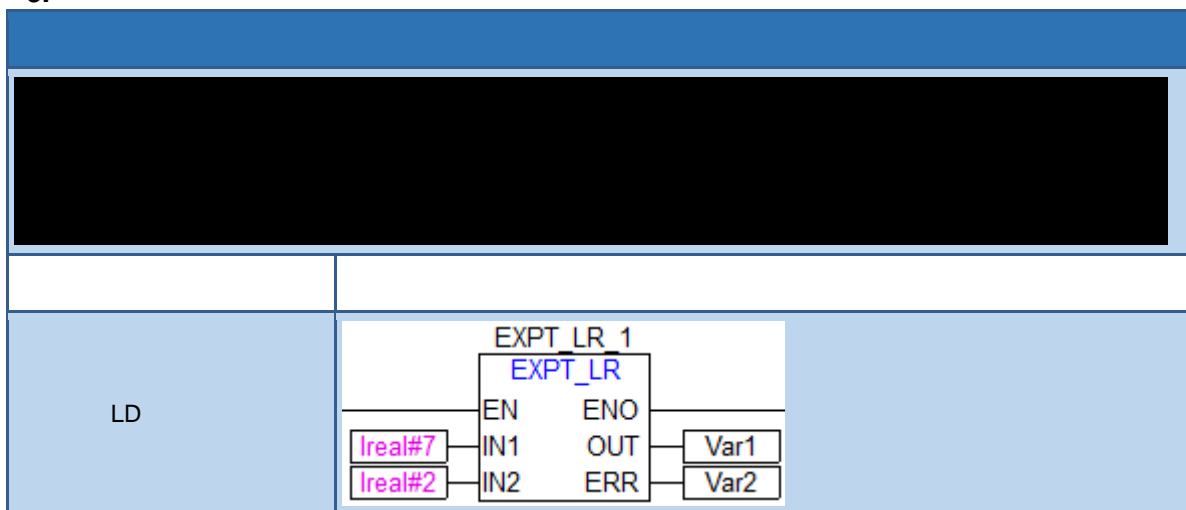
1.

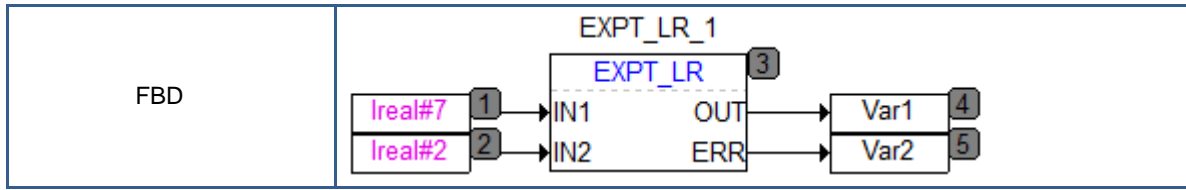
IN1	LREAL	1	0	[0, +
IN2	LREAL	2	0	
OUT	LREAL		0	
ERR	BYTE	ERR=0 ERR=1            IN1 OUT ERR=2            IN1=0 IN2<=0            OUT ERR=3            ln ln IN1  +ln ln2 >6.562    OUT	0	

2.



3.





### 2.2.1.46 MOVE



46

1.

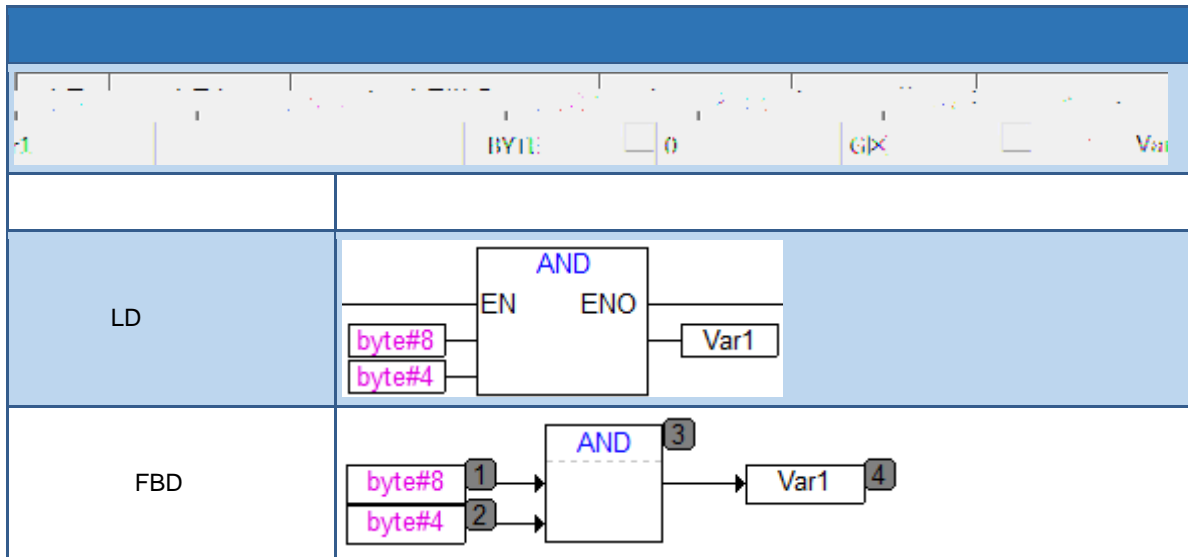


2		BOOL BYTE WORD DWORD
		BOOL BYTE WORD DWORD

	1	2	
0	0	0	
0	1	0	
1	0	0	
1	1	1	

2.

3.



2.2.2.2 OR



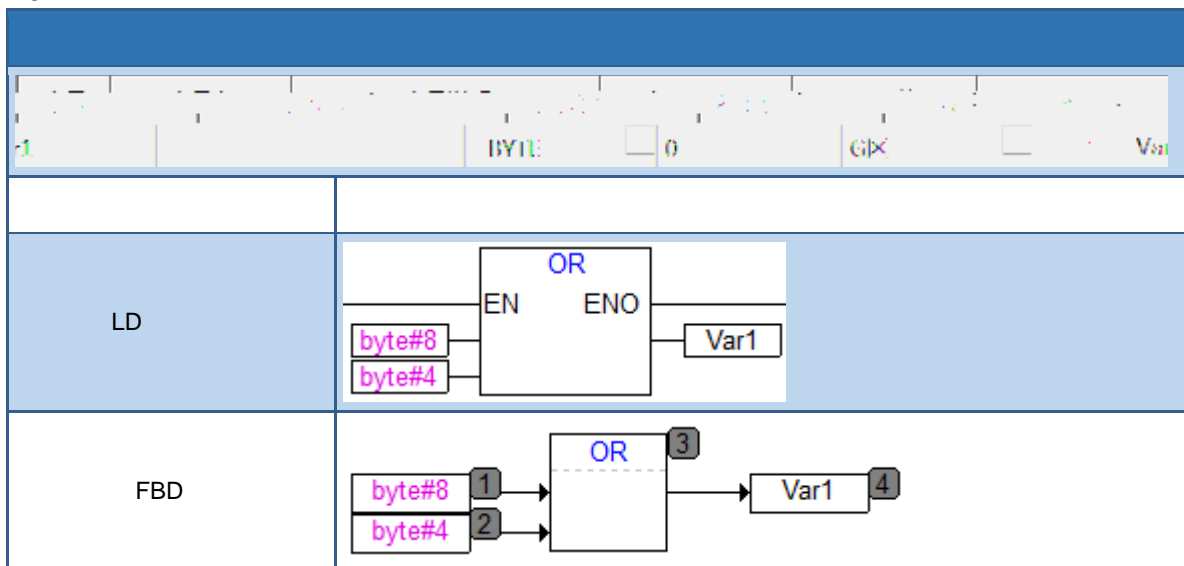
1.

1		BOOL BYTE WORD DWORD
2		BOOL BYTE WORD DWORD
		BOOL BYTE WORD DWORD

	1	2
0	0	0
0	1	1
1	0	1
1	1	1

2.

3.



### 2.2.2.3 XOR



49

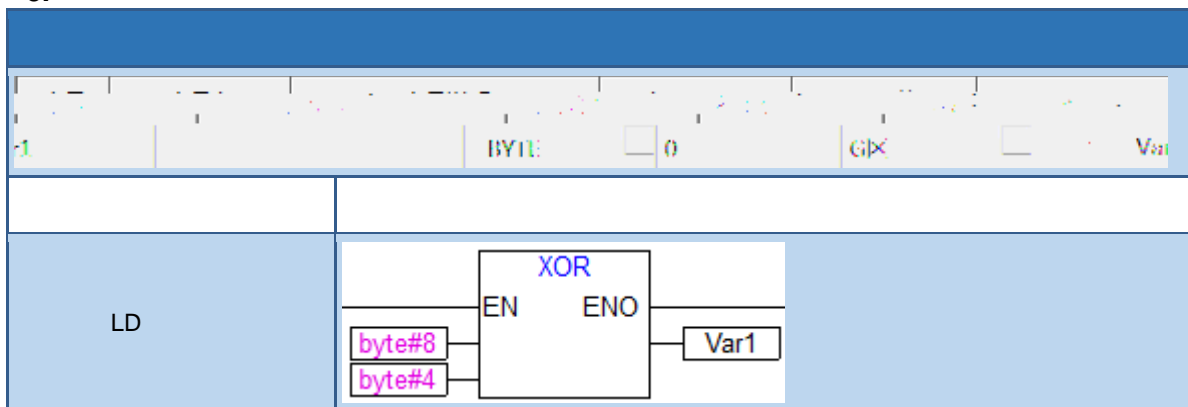
1.

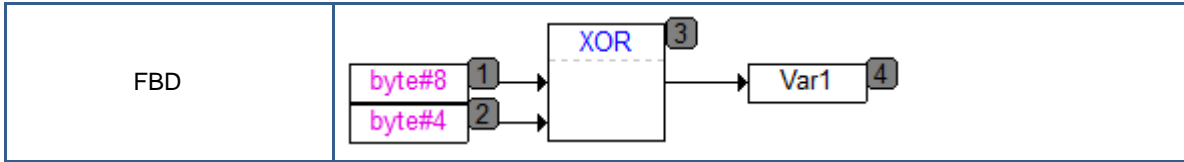
1		BOOL BYTE WORD DWORD
2		BOOL BYTE WORD DWORD
		BOOL BYTE WORD DWORD

	1	2	
0	0	0	0
0	1	1	1
1	0	1	1
1	1	0	0

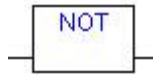
2.

3.





2.2.2.4 NOT



50

1.

		BOOL	BYTE WORD DWORD
		BOOL	BYTE WORD DWORD

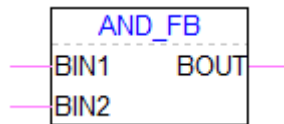
2.

3.

1
BYE 0 GX Var

LD	
FBD	

2.2.2.5 AND\_FB



51

1.



BIN1	1	BOOL
BIN2	2	BOOL
BOUT		BOOL

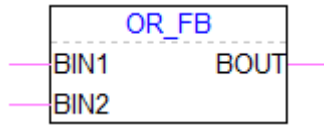
1	2	
0	0	0
0	1	0
1	0	0
1	1	1

2.

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	AND_FB_1		AND_FB		G区
0002	Var1		BOOL	FALSE	G区

### 2.2.2.6 OR\_FB



52

1.

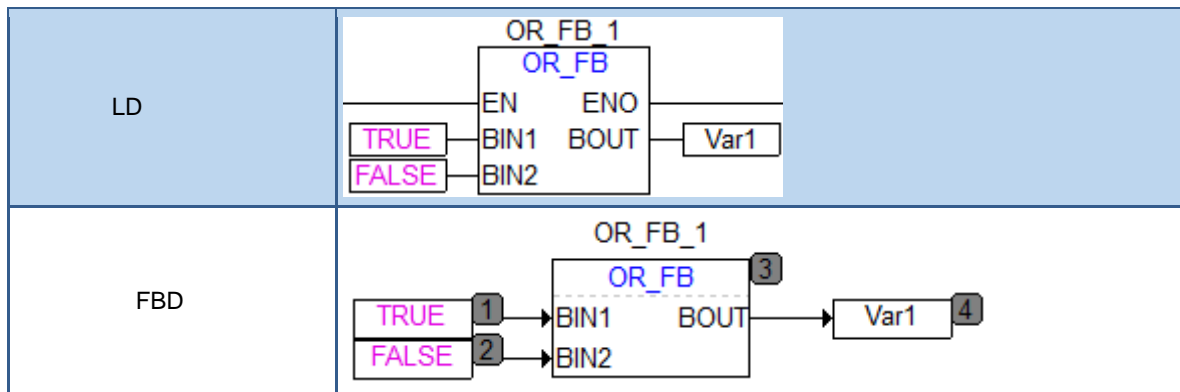
BIN1	1	BOOL
BIN2	2	BOOL
BOUT		BOOL

	1	2	
0	0	0	0
0	1	1	1
1	0	1	1
1	1	1	1

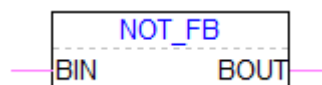
2.

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	OR_FB_1		OR_FB		G区
0002	Var1		BOOL	FALSE	G区



### 2.2.2.7 NOT\_FB



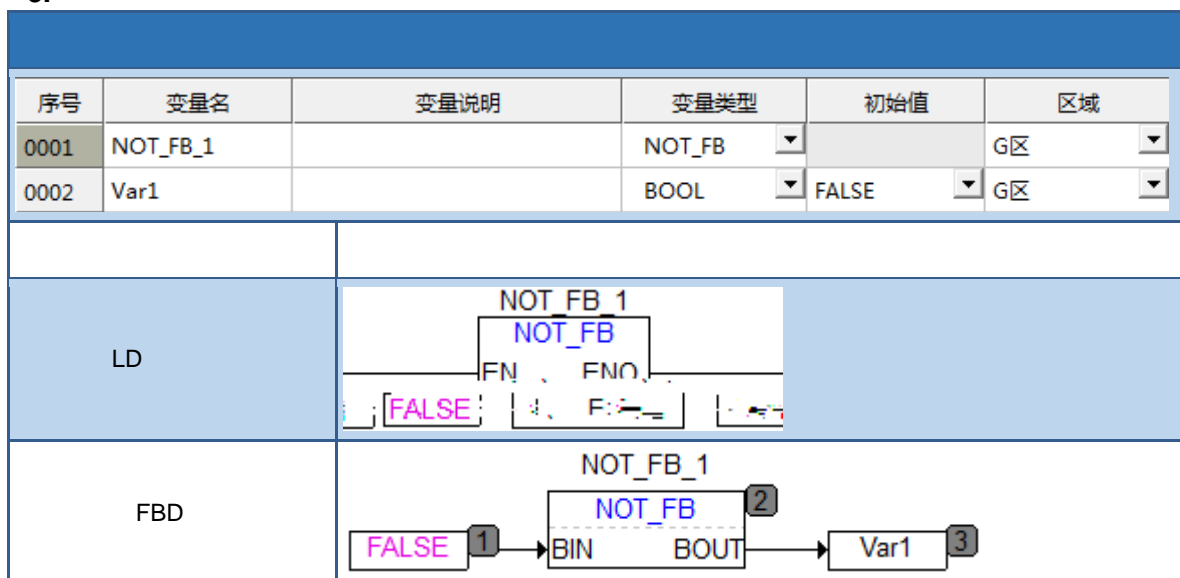
53

1.

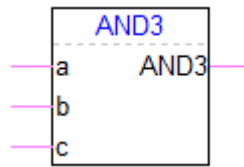
BIN		BOOL
BOUT		BOOL

2.

3.



## 2.2.2.8 AND3 3



54

1.

a	BOOL	a	0
b	BOOL	b	0
c	BOOL	c	0

2.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		BOOL	FALSE	G区

1.

a	BOOL	a	0
b	BOOL	b	0
c	BOOL	c	0

2.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		BOOL	FALSE	G区

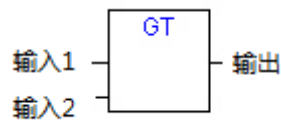
LD

FBD

## 2.2.3

### 2.2.3.1 GT



56

1.

1		BYTE WORD DWORD SINT INT DINT REAL LREAL TIME DATE TOD DT
2		BYTE WORD DWORD SINT INT DINT REAL LREAL TIME DATE TOD DT

		BOOL



2.

1            2            TURE  
 1            2            FALSE

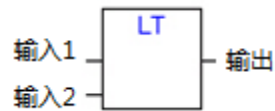
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		BOOL	FALSE	G区

LD	
FBD	

2.2.3.2 LT



57

1.

1		BYTE	WORD	DWORD	SINT	INT	DINT	REAL	LREAL	TIME	DATE	TOD	DT
2		BYTE	WORD	DWORD	SINT	INT	DINT	REAL	LREAL	TIME	DATE	TOD	DT
		BOOL											



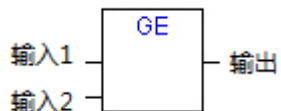
2.

1	2	TURE
1	2	FALSE

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		BOOL	FALSE	G区
LD					
FBD					

### 2.2.3.3 GE



58

1.

1	BYTE WORD DWORD SINT INT DINT REAL LREAL TIME DATE TOD DT
2	BYTE WORD DWORD SINT INT DINT REAL LREAL TIME DATE TOD DT
	BOOL



2.

1                    2            TURE  
 1                    2            FALSE

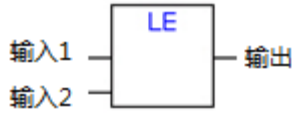
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		BOOL	FALSE	G区

LD	
FBD	

2.2.3.4 LE



59

1.

1	BYTE WORD DWORD SINT INT DINT REAL LREAL TIME DATE TOD DT
2	BYTE WORD DWORD SINT INT DINT REAL LREAL TIME DATE TOD DT
	BOOL



2.

1                    2            TURE

1            2            FALSE

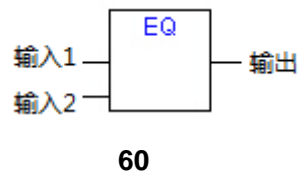
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		BOOL	FALSE	G区

LD	
FBD	

### 2.2.3.5 EQ



1.

1		BOOL BYTE WORD DWORD SINT INT DINT TIME DATE TOD DT
2		BOOL BYTE WORD DWORD SINT INT DINT TIME DATE TOD DT
		BOOL



2.

1            2            TURE  
 1            2            FALSE

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		BOOL	FALSE	G区

LD	
FBD	

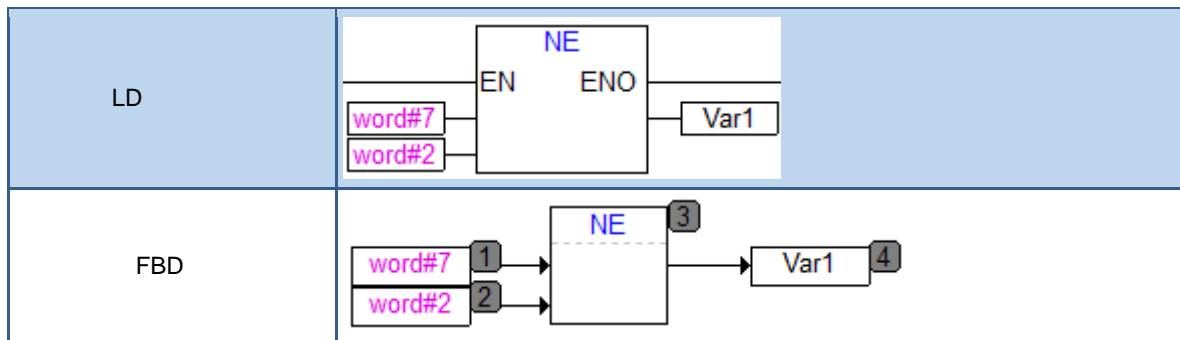
2.2.3.6 NE



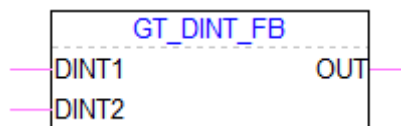
61

1. DT

1	BOOL BYTE WORD DWORD SINT INT DINT TIME DATE TOD DT
---	---



### 2.2.3.7 GT\_DINT\_FB



62

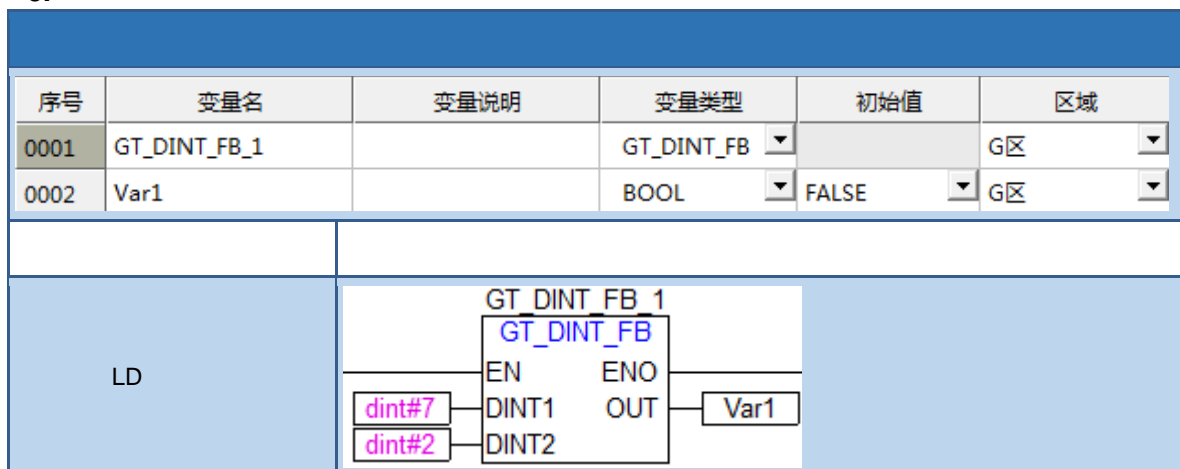
1.

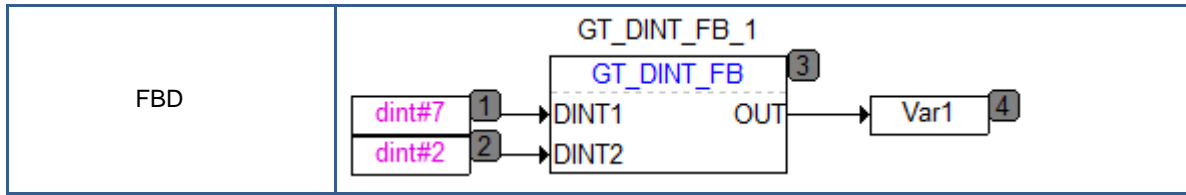
名称	地址	数据类型
DINT1	1	DINT
DINT2	2	DINT
OUT		BOOL

2.

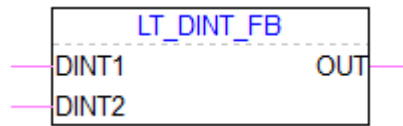
1            2            TURE  
 1            2            FALSE

3.





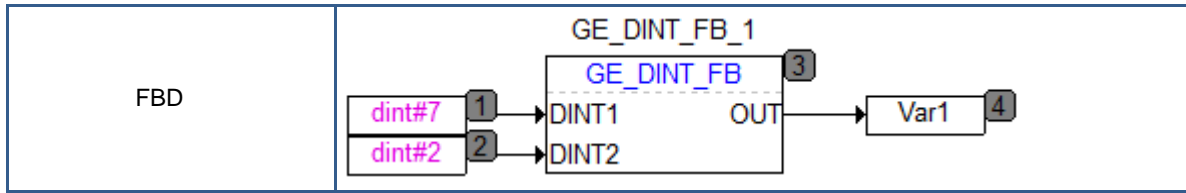
### 2.2.3.8 LT\_DINT\_FB



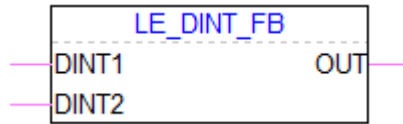
63

1.





2.2.3.10 LE\_DINT\_FB



65

1.

DINT1	1	DINT
DINT2	2	DINT
OUT		BOOL

2.

1            2            TURE  
 1            2            FALSE

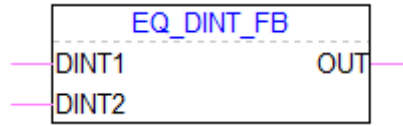
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	LE_DINT_FB_1		LE_DINT_FB		G区
0002	Var1		BOOL	FALSE	G区

LD	
FBD	

### 2.2.3.11 EQ\_DINT\_FB



66

1.

DINT1	1	DINT
DINT2	2	DINT
OUT		BOOL

2.

1        2        TURE  
 1        2        FALSE

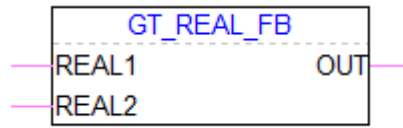
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	EQ_DINT_FB_1		EQ_DINT_FB		G区
0002	Var1		BOOL	FALSE	G区

LD	<p>Diagram showing the EQ_DINT_FB block in a Ladder Logic (LD) context. The block is labeled EQ_DINT_FB_1. Its EN input is connected to a normally open contact labeled dint#7. Its DINT1 input is connected to a normally open contact labeled dint#2. Its DINT2 input is connected to a normally open contact labeled dint#2. Its OUT output is connected to a coil labeled Var1.</p>
FBD	<p>Diagram showing the EQ_DINT_FB block in a Function Block Diagram (FBD) context. The block is labeled EQ_DINT_FB_1. Its DINT1 input is connected to a terminal block labeled 1, which is also labeled dint#7. Its DINT2 input is connected to a terminal block labeled 2, which is also labeled dint#2. Its OUT output is connected to a terminal block labeled 3, which is also labeled EQ_DINT_FB_1. This terminal block 3 is connected to a coil labeled Var1, which is also labeled 4.</p>

### 2.2.3.12 GT\_REAL\_FB



67

1.

REAL1		REAL
REAL2		REAL
OUT		BOOL

2.

1            2            TURE  
 1            2            FALSE

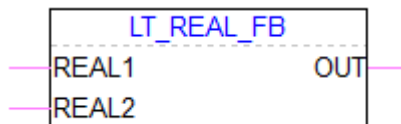
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	GT_REAL_FB_1		GT_REAL_FB		G区
0002	Var1		BOOL	FALSE	G区

LD	<p>Diagram showing the LD (Ladder Logic) representation of the function block. The block is labeled GT_REAL_FB_1 and contains the sub-block GT_REAL_FB. Inputs real#7 and real#2 are connected to REAL1 and REAL2 respectively. The output OUT is connected to Var1.</p>
FBD	<p>Diagram showing the FBD representation of the function block. The block is labeled GT_REAL_FB_1 and contains the sub-block GT_REAL_FB. Input real#7 is connected to REAL1 (labeled 1), input real#2 is connected to REAL2 (labeled 2), and the output OUT is connected to Var1 (labeled 4). A small box with the number 3 is placed above the output line.</p>

### 2.2.3.13 LT\_REAL\_FB



68

1.

REAL1		REAL
REAL2		REAL
OUT		BOOL

2.

1            2            TURE  
 1            2            FALSE

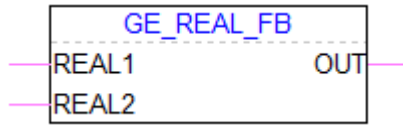
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	LT_REAL_FB_1		LT_REAL_FB		G区
0002	Var1		BOOL	FALSE	G区

LD	
FBD	

### 2.2.3.14 GE\_REAL\_FB



69

1.

REAL1		REAL
REAL2		REAL
OUT		BOOL

2.

1                    2            TURE  
 1                    2            FALSE

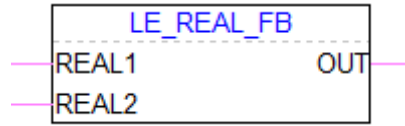
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	GE_REAL_FB_1		GE_REAL_FB		G区
0002	Var1		BOOL	FALSE	G区

LD	
FBD	

### 2.2.3.15 LE\_REAL\_FB



70

1.

REAL1		REAL
REAL2		REAL
OUT		BOOL

2.

1                    2            TURE  
 1                    2            FALSE

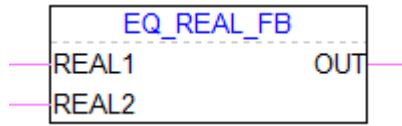
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	LE_REAL_FB_1		LE_REAL_FB		G区
0002	Var1		BOOL	FALSE	G区

LD	<p>Diagram showing the LE_REAL_FB block in a Ladder Logic (LD) context. The block is labeled 'LE REAL FB 1' and 'LE_REAL_FB'. It has two inputs: 'real#7' connected to 'REAL1' and 'real#2' connected to 'REAL2'. The output 'OUT' is connected to a variable box labeled 'Var1'. The 'EN' and 'ENO' ports are also shown.</p>
FBD	<p>Diagram showing the LE_REAL_FB_1 block in a Function Block Diagram (FBD) context. The block is labeled 'LE_REAL_FB_1' and 'LE_REAL_FB'. It has two inputs: 'real#7' (labeled 1) connected to 'REAL1' and 'real#2' (labeled 2) connected to 'REAL2'. The output 'OUT' (labeled 3) is connected to a variable box labeled 'Var1' (labeled 4).</p>

### 2.2.3.16 EQ\_REAL\_FB



71

1.

REAL1		REAL
REAL2		REAL
OUT		BOOL

2.

1	2	TURE
1	2	FALSE

3.

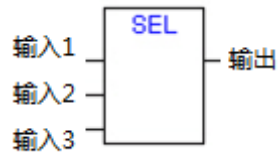
序号	变量名	变量说明	变量类型	初始值	区域
0001	EQ_REAL_FB_1		EQ_REAL_FB		G区
0002	Var1		BOOL	FALSE	G区

LD	<p>Diagram showing a normally open contact labeled EQ_REAL_FB_1. The contact is connected to a coil labeled EQ_REAL_FB. The coil has two inputs labeled real#7 and real#2.</p>
FBD	<p>Diagram showing a function block EQ_REAL_FB_1 with two inputs labeled real#7 (1) and real#2 (2). The block has an output labeled OUT (3) which is connected to a variable Var1 (4).</p>

## 2.2.4

### 2.2.4.1 SEL



72

1.

1		BOOL
2		
3		
	1 FALSE 2 1 TRUE	

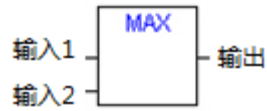
2.

1 FALSE 2 1 TRUE 3

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		WORD	0	G区
LD	<p>The LD diagram shows a SEL block with three inputs on the left: 'TRUE', 'word#7', and 'word#2'. The EN input is connected to the top. The ENO output is connected to a box labeled 'Var1'.</p>				
FBD	<p>The FBD diagram shows a SEL block with three inputs on the left, numbered 1, 2, and 3. Input 1 is 'TRUE', input 2 is 'word#7', and input 3 is 'word#2'. The output of the SEL block is numbered 4 and is connected to a box labeled 'Var1' which is numbered 5.</p>				

### 2.2.4.2 MAX



73

1.

序号	变量名	变量说明	变量类型	初始值	区域
1		BOOL			
2		BOOL			

2.

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		WORD	0	G区
LD		<p>The diagram shows a normally closed contact labeled 'LD'. The contact contains a 'MAX' block. The 'MAX' block has two inputs: 'word#7' and 'word#2'. The output of the 'MAX' block is connected to a coil labeled 'Var1'. The 'MAX' block also has 'EN' and 'ENO' labels.</p>			
FBD		<p>The diagram shows a 'MAX' block with two inputs labeled 'word#7' (1) and 'word#2' (2). The output of the 'MAX' block is labeled '3' and is connected to a coil labeled 'Var1' (4).</p>			

### 2.2.4.3 MIN



74

1.

序号	变量名	变量说明	变量类型	初始值	区域
1		BOOL			
2		BOOL			

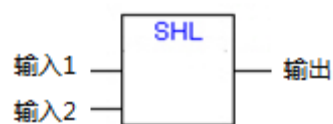
2.

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		WORD	0	G区
LD					
FBD					

## 2.2.5

### 2.2.5.1 SHL



75

1.

序号	变量名	变量说明	变量类型	初始值	区域
1		DWORD DINT			
2		BYTE			

		DWORD DINT

2.

0

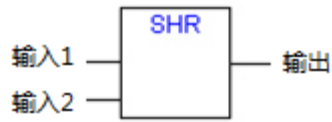
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		DWORD	0	G区

LD	
FBD	

### 2.2.5.2 SHR



76

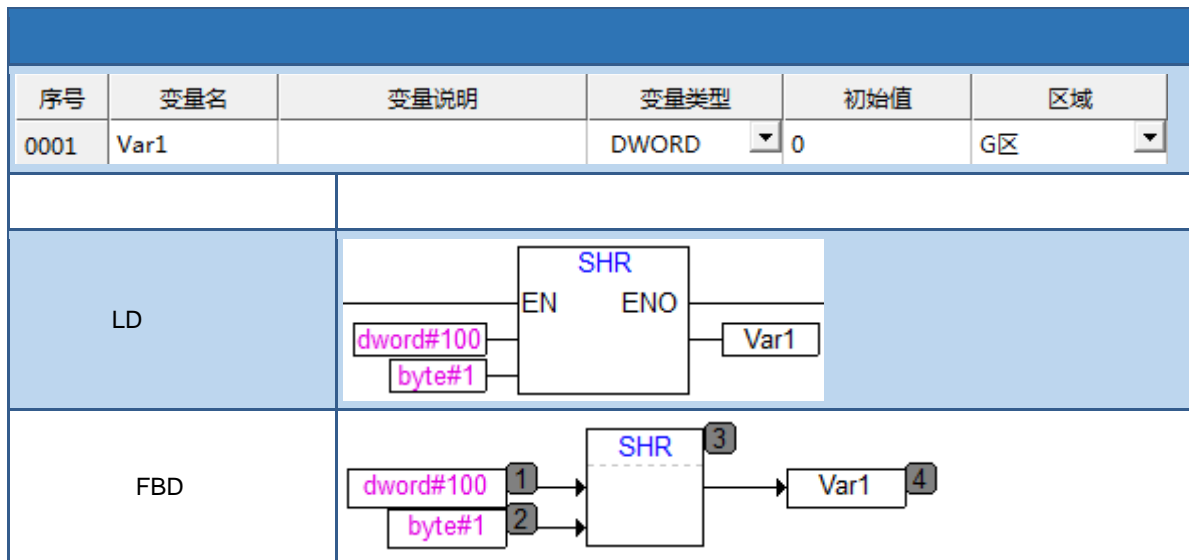
1.

1		DWORD DINT
2		BYTE
		DWORD DINT

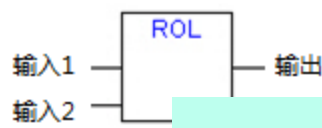
2.

0

3.



### 2.2.5.3 ROL

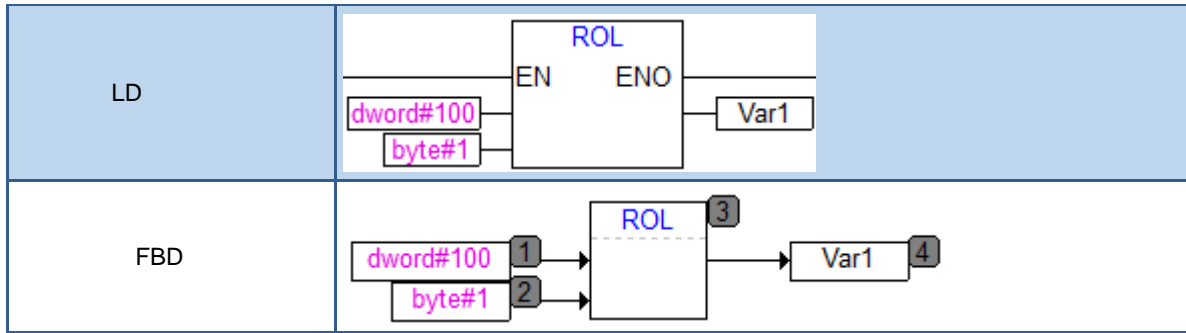


77

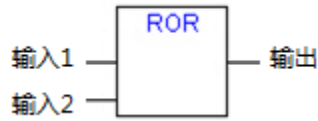
1.

1		DWORD DINT
2		BYTE

DWORD DINT



### 2.2.5.4 ROR



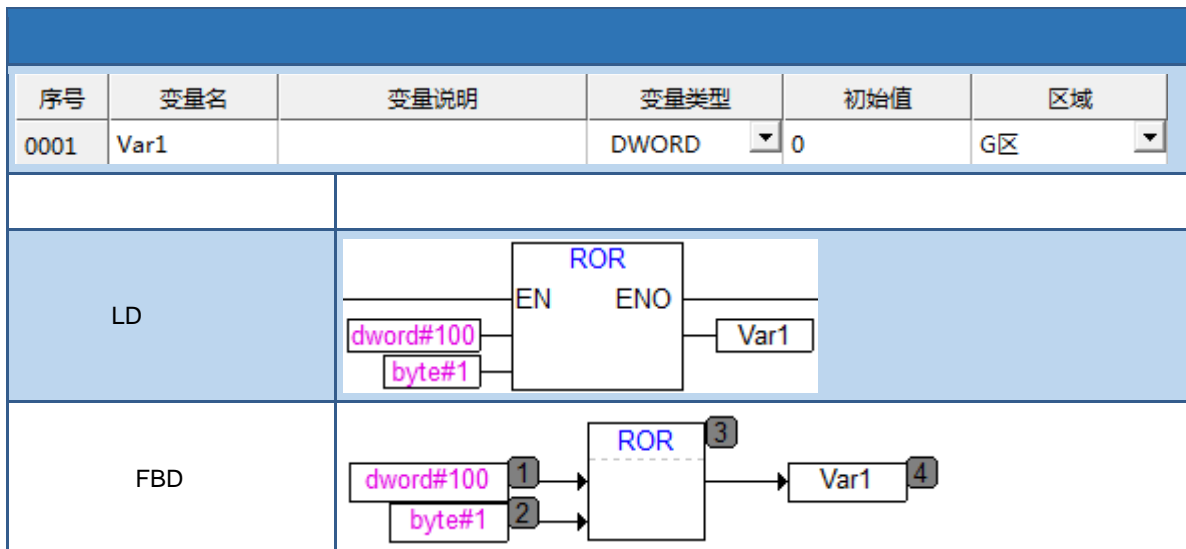
78

1.

1		DWORD DINT
2		BYTE
		DWORD DINT

2.

3.



## 2.2.6

### 2.2.6.1 BYTE\_TO BYTE

1.

BYTE

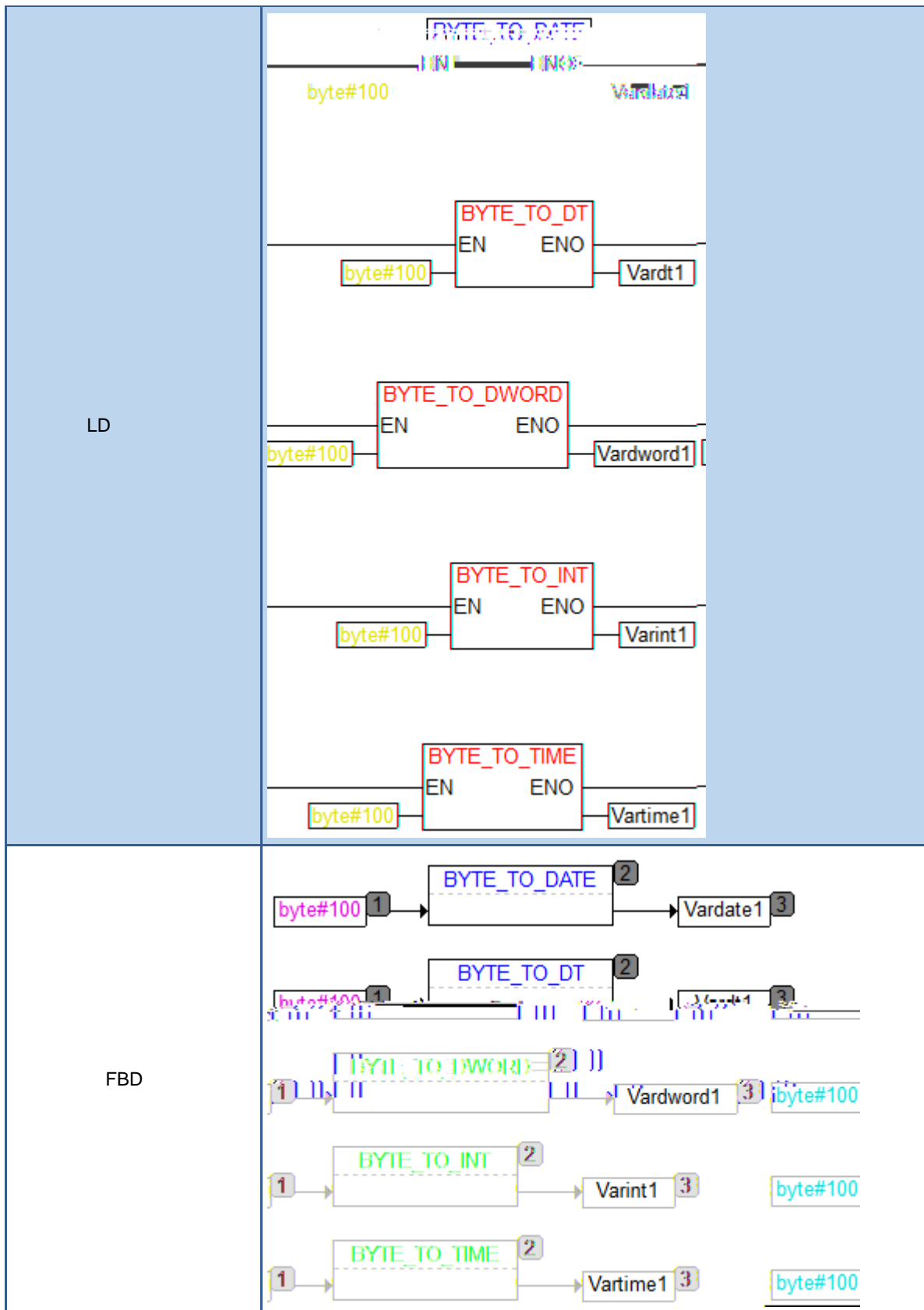
2.

		BYTE
		DATE DINT DT DWORD INT SINT TIME TOD WORD

3.

4.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Vardate1		DATE ▼	D#1970-01-01	G区 ▼
0002	Vardt1		DT ▼	DT#1970-01-0...	G区 ▼
0003	Vardword1		DWORD ▼	0	G区 ▼
0004	Varint1		INT ▼	0	G区 ▼
0005	Vartime1		TIME ▼	T#0MS	G区 ▼



### 2.2.6.2 WORD\_TO WORD

1.

WORD

2.

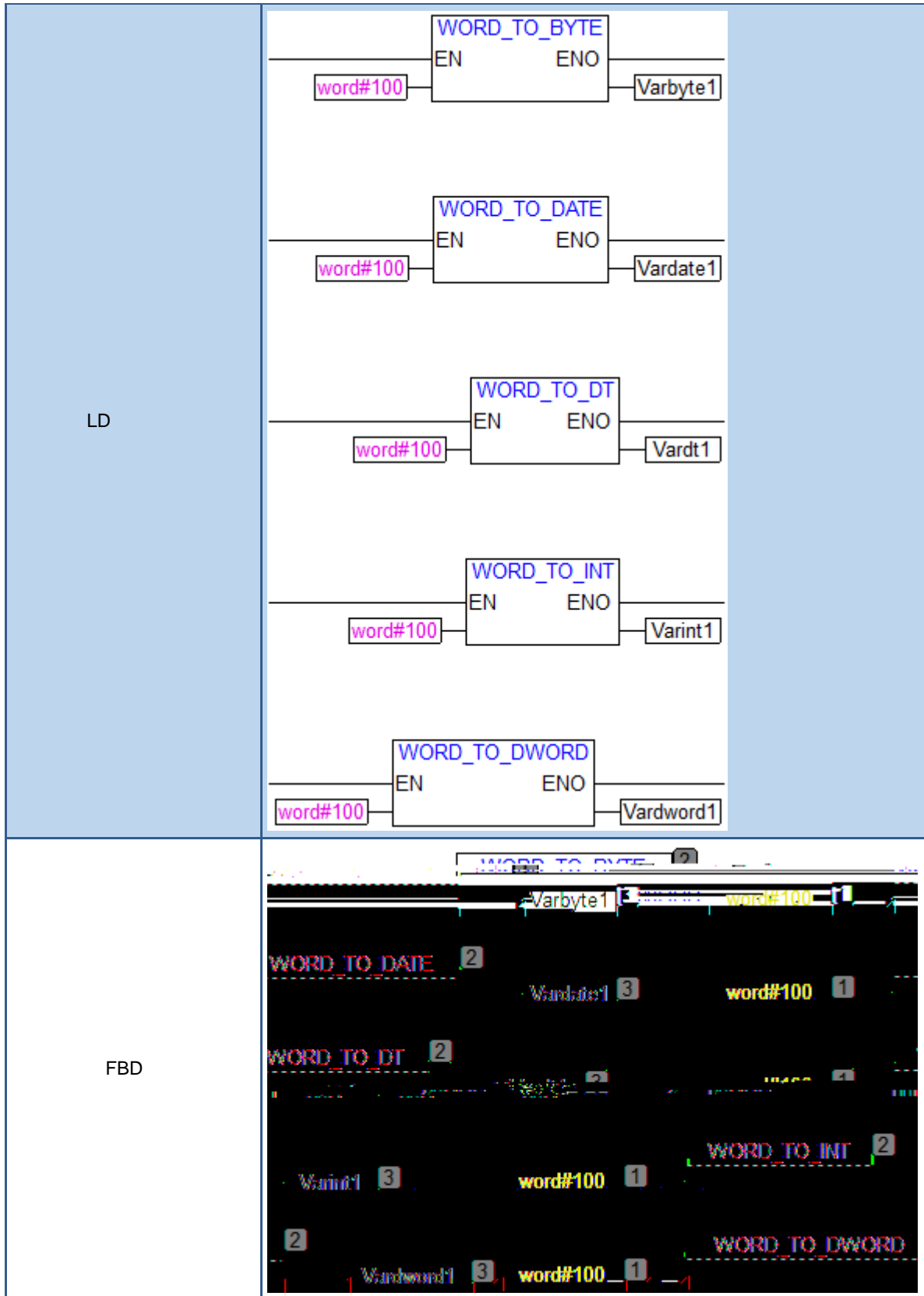
		WORD
		BYTE DATE DINT DT DWORD INT SINT TIME TOD

3.

WORD\_TO\_XXX XXX

4.

	DATE	D#1970-01-01	GE	VarDate1	
	DT	DT#1970-01-0...	GE	VarDt1	
	DWORD	0	GE	VarDword1	
	INT	0	GE	VarInt1	
	BYTE	0	GE	VarByte1	



### 2.2.6.3 DWORD\_TO DWORD

1.

DWORD

2.

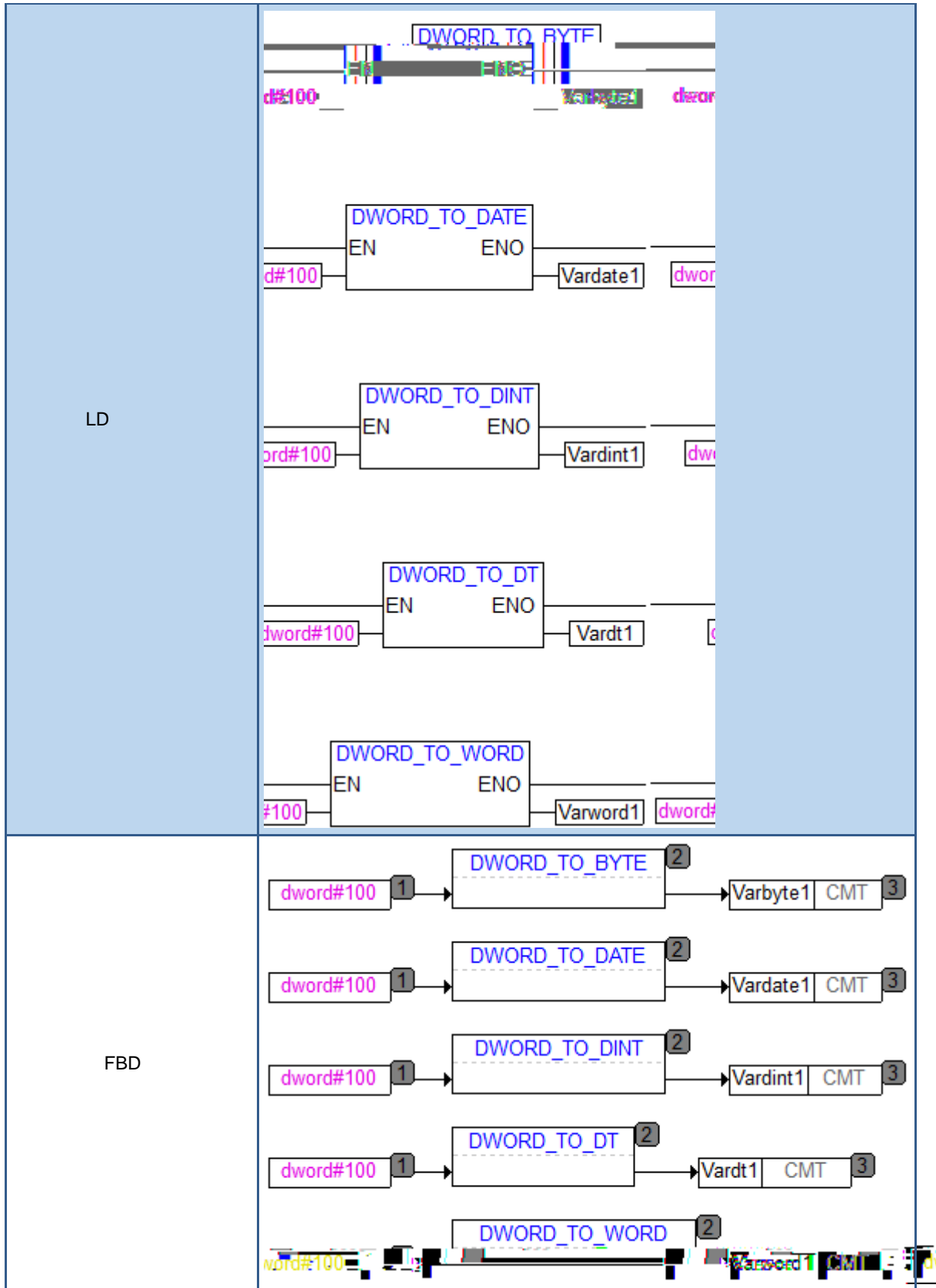
		DWORD
		BYTE DATE DINT DT INT LREAL REAL SINT TIME TOD WORD

3.

DWORD\_TO\_XXX xxx

4.

序号	变量名	变量说明	变量类型	初始值	区域
date1		DATE	D#19/0-01-01	GP	0001 Var
dt1		DT	DT#19/0-01-01-0...	GP	0002 Var
byte1		BYTE	0	GP	0003 Var
dint1		DINT	0	GP	0004 Var
word1		WORD	0	GP	0005 Var



### 2.2.6.4 SINT\_TO SINT

1.

SINT

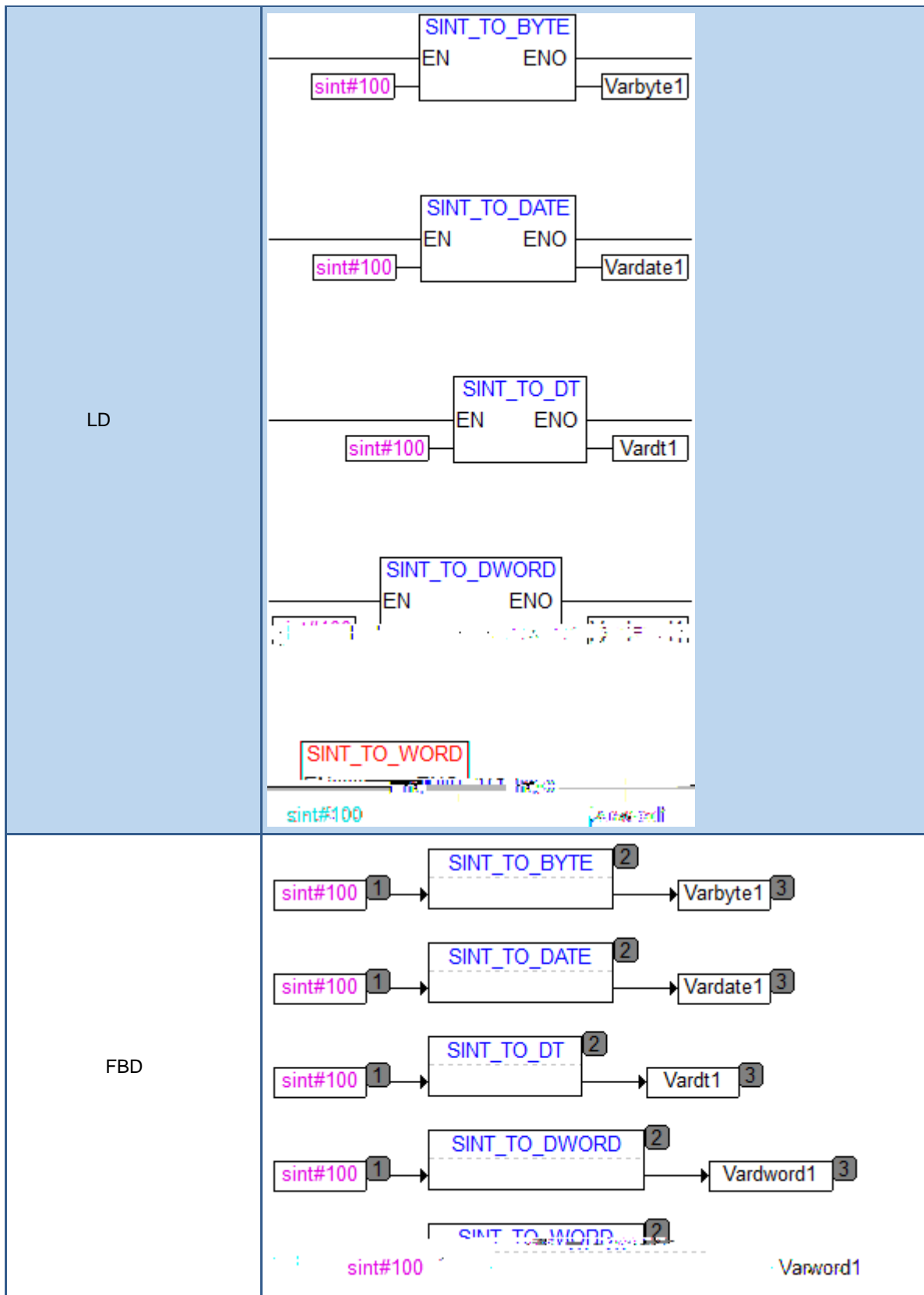
2.

		SINT
		BYTE DATE DINT DT DWORD INT TIME TOD WORD

3.

SINT\_TO\_xxx xxx

4.

### 2.2.6.5 INT\_TO INT

1.

INT

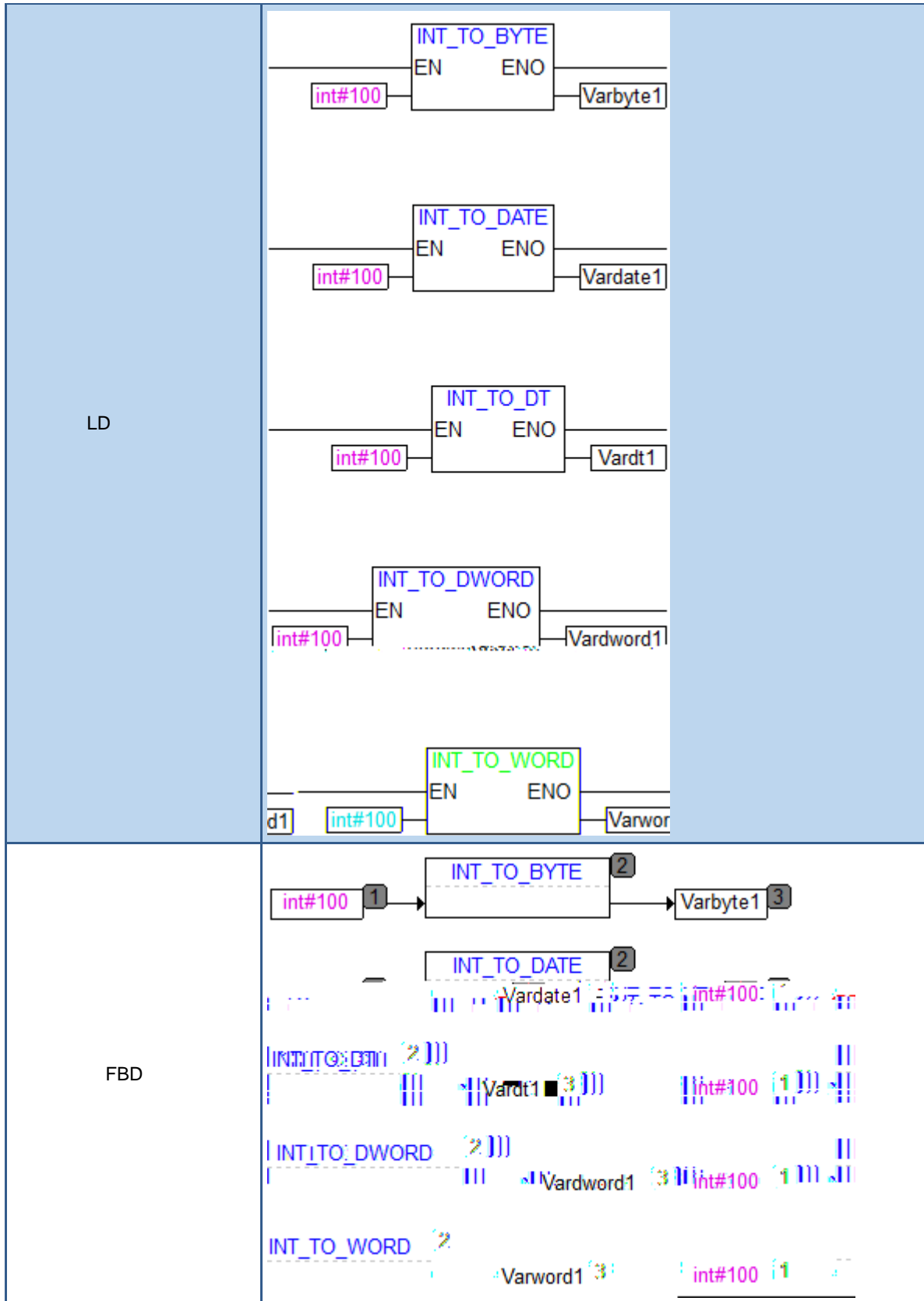
2.

		INT
		BYTE DATE DINT DT DWORD SINT TIME TOD WORD

3.

INT\_TO\_XXX XXX

4.



### 2.2.6.6 DINT\_TO DINT

1.

DINT

2.

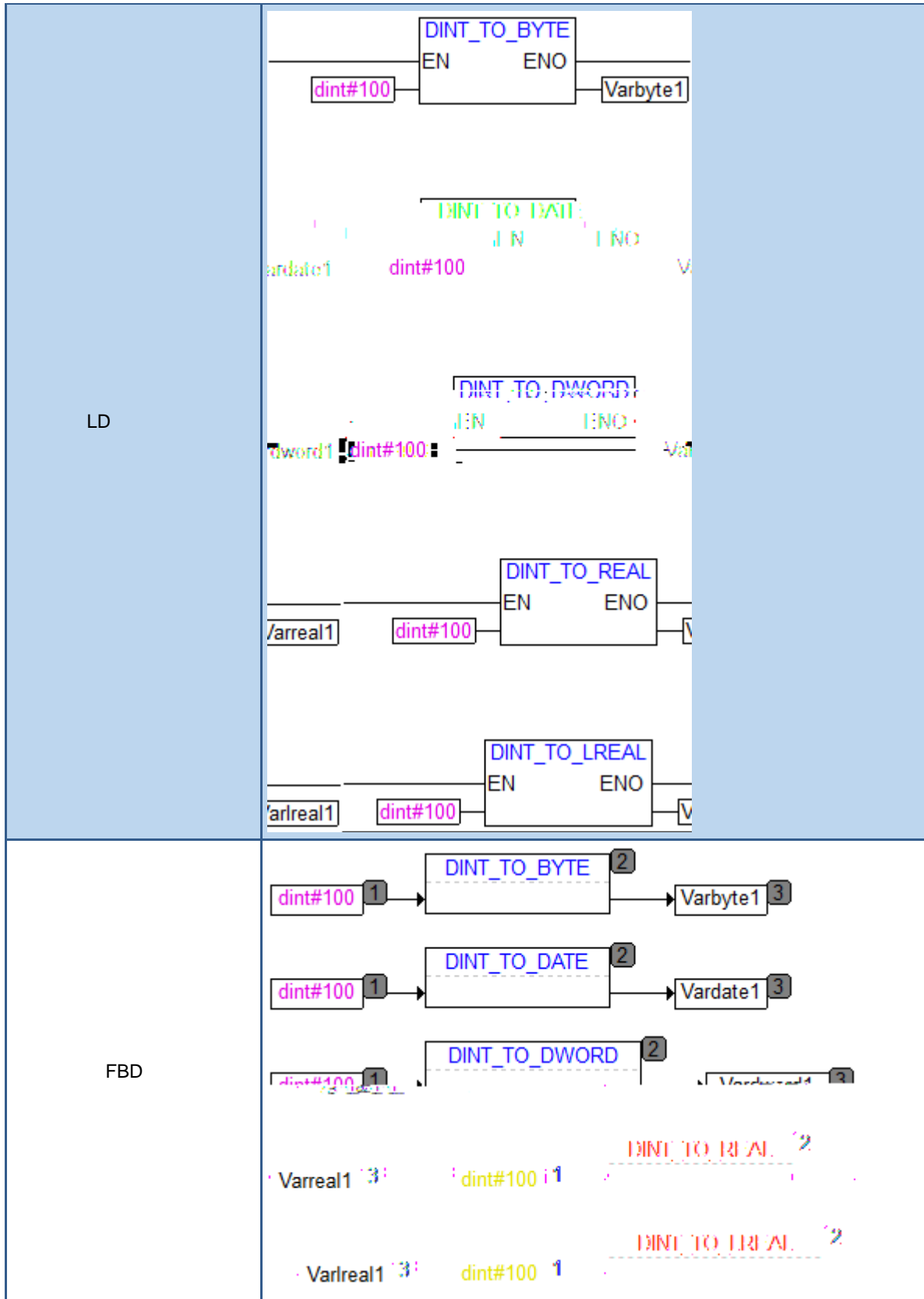
		DINT
		BYTE DATE DT DWORD INT LREAL REAL SINT TIME TOD WORD

3.

DINT\_TO\_XXX    xxx

4.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Vardate1		DATE ▾	D#1970-01-01	G区 ▾
0002	Varbyte1		BYTE ▾	0	G区 ▾
0003	Vardword1		DWORD ▾	0	G区 ▾
0004	Varreal1		REAL ▾	0	G区 ▾
0005	Varlreal1		LREAL ▾	0	G区 ▾



### 2.2.6.7 REAL/LREAL\_TO REAL/LREAL

1.

REAL/LREAL

2.

		REAL/LREAL					
		DATE DINT DT DWORD TIME TOD REAL LREAL LREAL REAL					

3.

REAL/LREAL

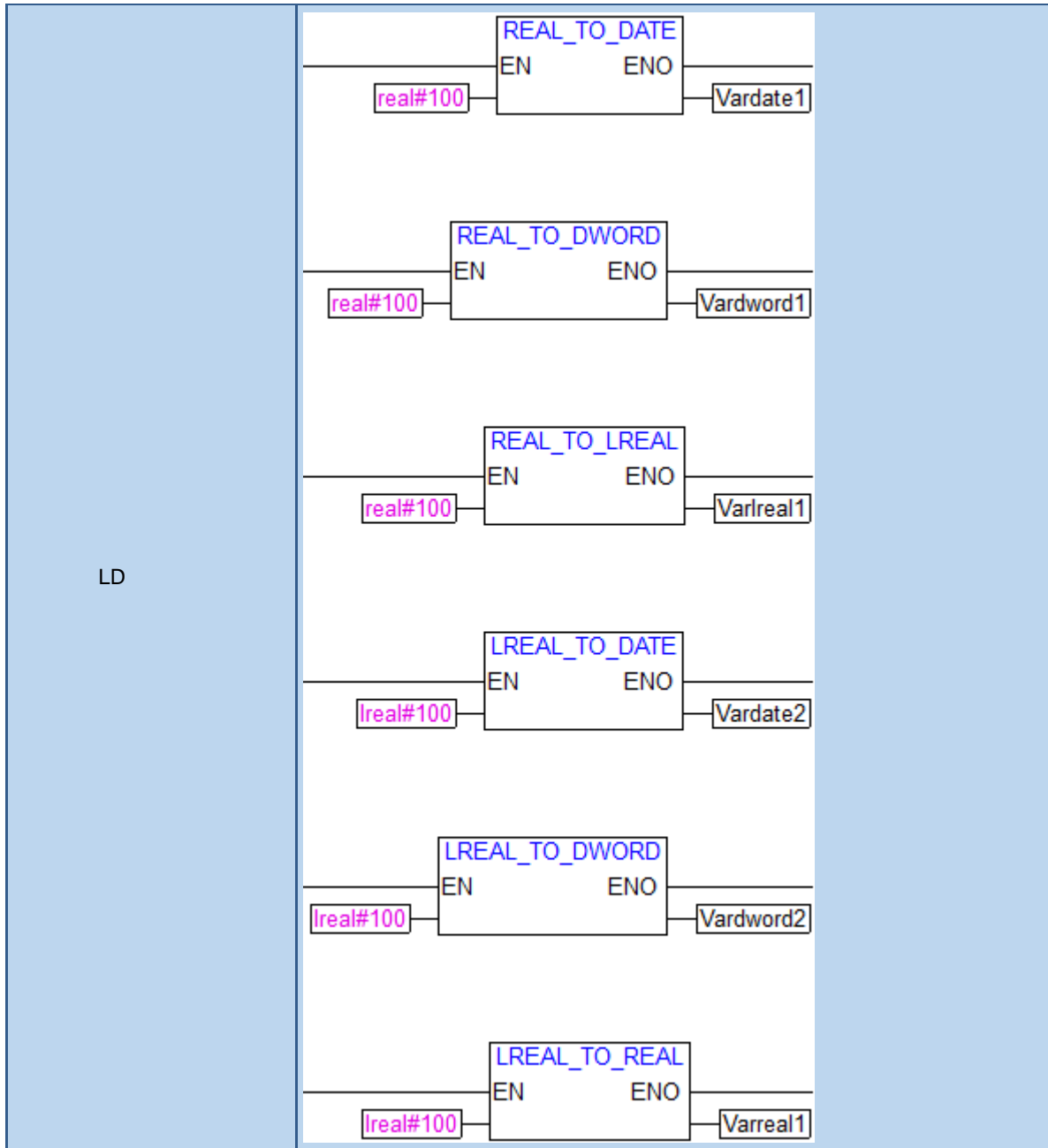
REAL\_TO/LREAL\_TO\_XXX

XXX



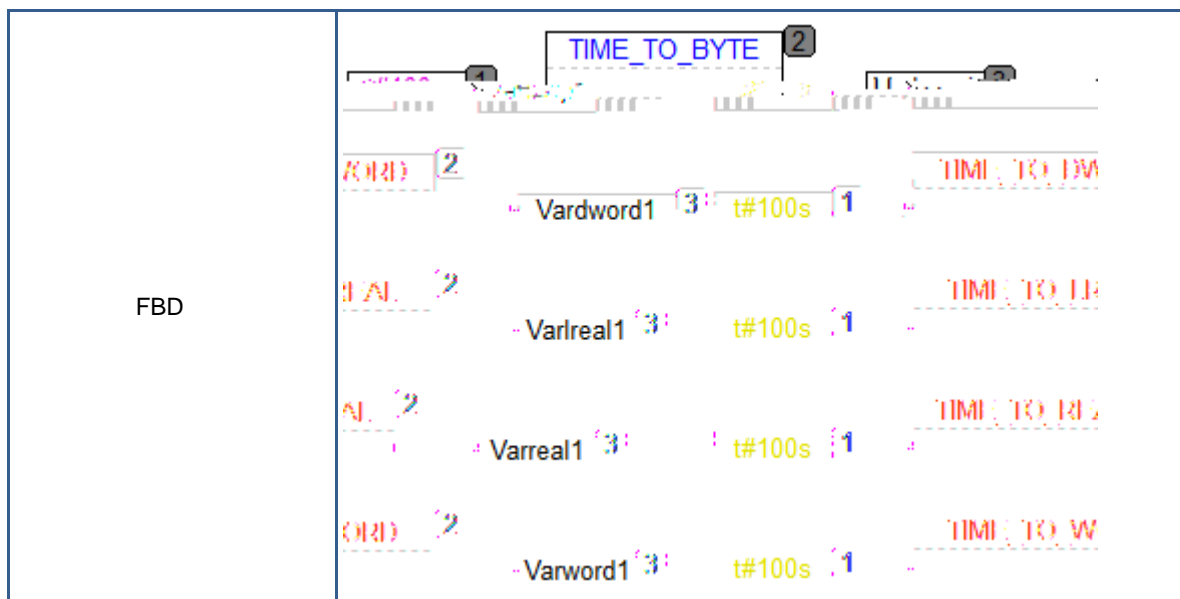
4.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Vardate1		DATE	D#1970-01-01	G区
0002	Vardword1		DWORD	0	G区
0003	Varreal1		REAL	0	G区
0004	Varreal1		LREAL	0	G区
0005	Vardate2		DATE	D#1970-01-01	G区
0006	Vardword2		DWORD	0	G区





序号	变量名	变量说明	变量类型	初始值	区域
0001	Varbyte1		BYTE	0	G区
0002	Varword1		DWORD	0	G区



### 2.2.6.9 DATE\_TO DATE

1.

DATE

2.

		DATE
		BYTE DINT DWORD INT LREAL REAL SINT WORD

3.

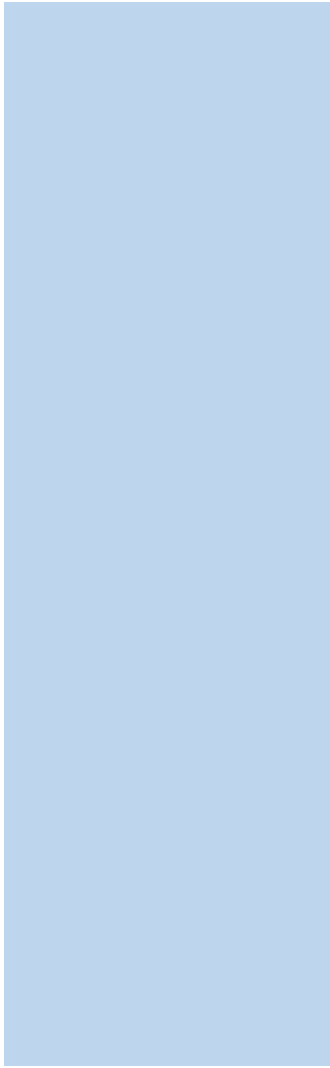
DATE

DATE\_TO\_XXX XXX

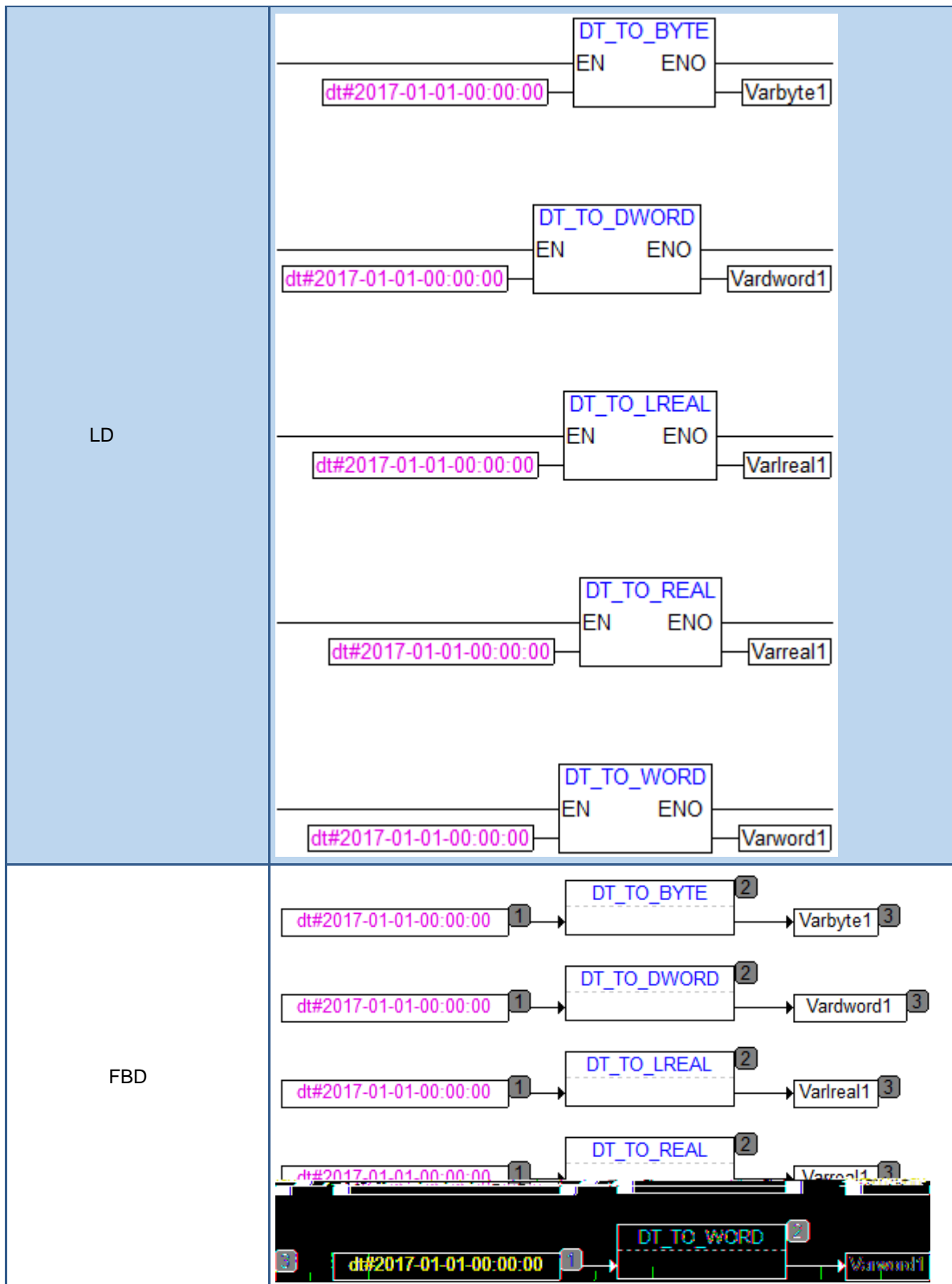
DATE

4.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Varbyte1		BYTE	0	G区
0002	Varword1		DWORD	0	G区







### 2.2.6.11 TOD\_TO TOD

- 1.

TOD

2.

		TOD
		BYTE DINT DWORD INT LREAL REAL SINT WORD

3.

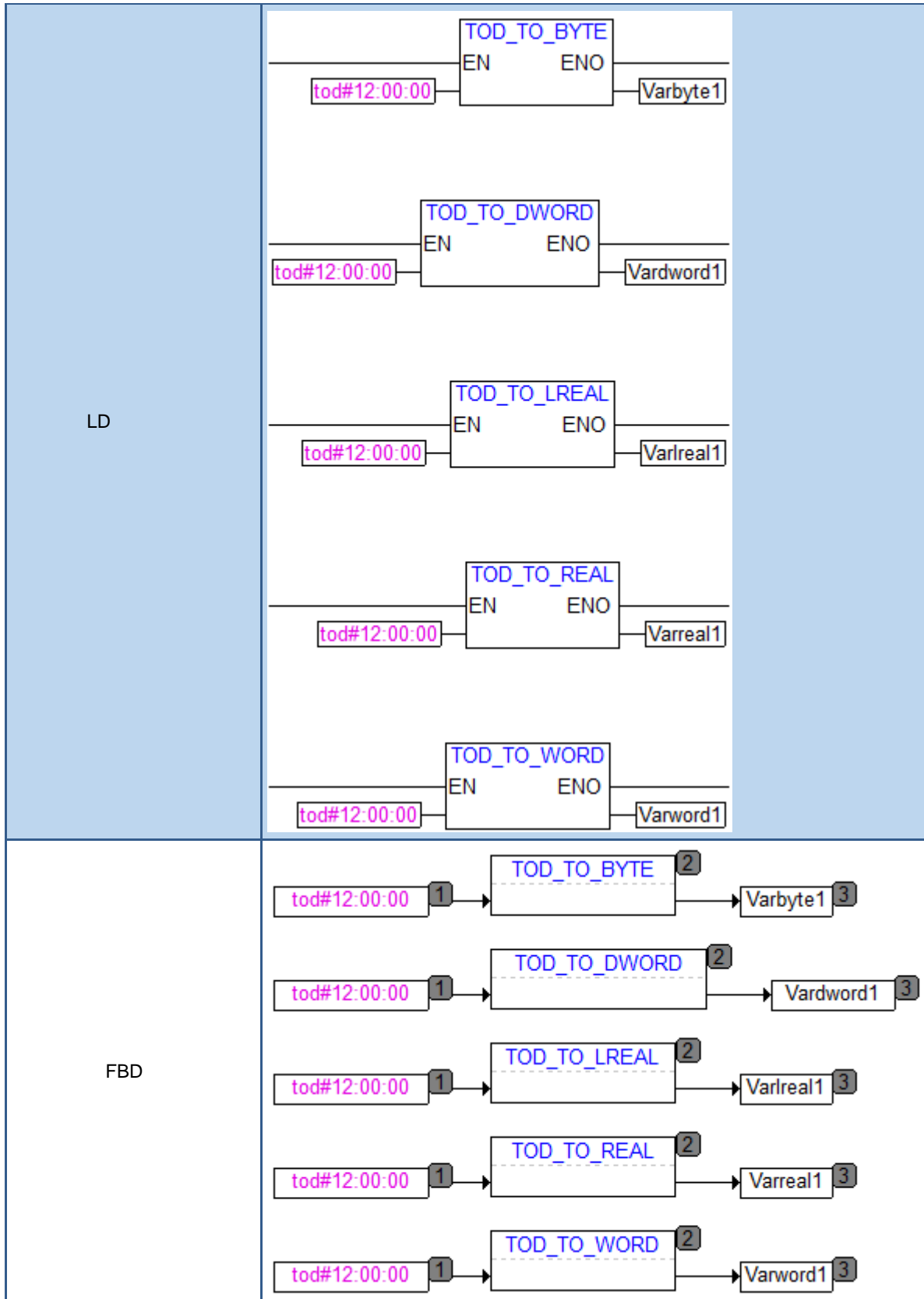
TOD

TOD\_TO\_xxx xxx

TOD

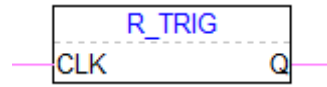
4.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Varbyte1		BYTE	0	G区
0002	Var dword1		DWORD	0	G区



## 2.2.7

### 2.2.7.1 R\_TRIG



79

1.



1.

CLK	BOOL		0
Q	BOOL		0

2.

	F_TRIG	CLK	CLK	Q	BOOL
Q	CLK	TRUE	FALSE	Q	TRUE
	FALSE				

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	F_TRIG_1		F_TRIG		G区
0002	Q		BOOL	FALSE	G区

Q	BOOL		0

Reset	Set	Q(n+1)
0	0	Q(n)
0	1	1
1	0	0
1	1	1

2.

SR AND Q) OR Set      Set Reset      Q      BOOL      Q =(NOT Reset RS

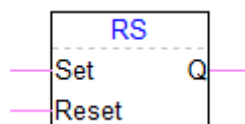
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	SR_1		SR		G区
0002	Var1		BOOL	FALSE	G区
0003	Var2		BOOL	FALSE	G区
0004	Var3		BOOL	FALSE	G区

LD	
FBD	

### 2.2.8.2 RS



82

1.

Set	BOOL		0
Reset	BOOL		0
Q	BOOL		0

Reset	Set	Q(n+1)
0	0	Q(n)
0	1	1
1	0	0
1	1	0

2.

RS                      Set Reset                      Q                      BOOL                      Q =NOT Reset  
 AND (Q OR Set)                      RS

3.

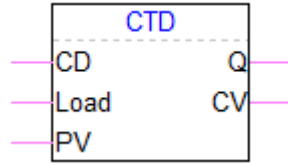
序号	变量名	变量说明	变量类型	初始值	区域
0001	RS_1		RS		G区
0002	Var1		BOOL	FALSE	G区
0003	Var2		BOOL	FALSE	G区
0004	Var3		BOOL	FALSE	G区

LD	
FBD	

## 2.2.9

### 2.2.9.1 CTD



83

1.

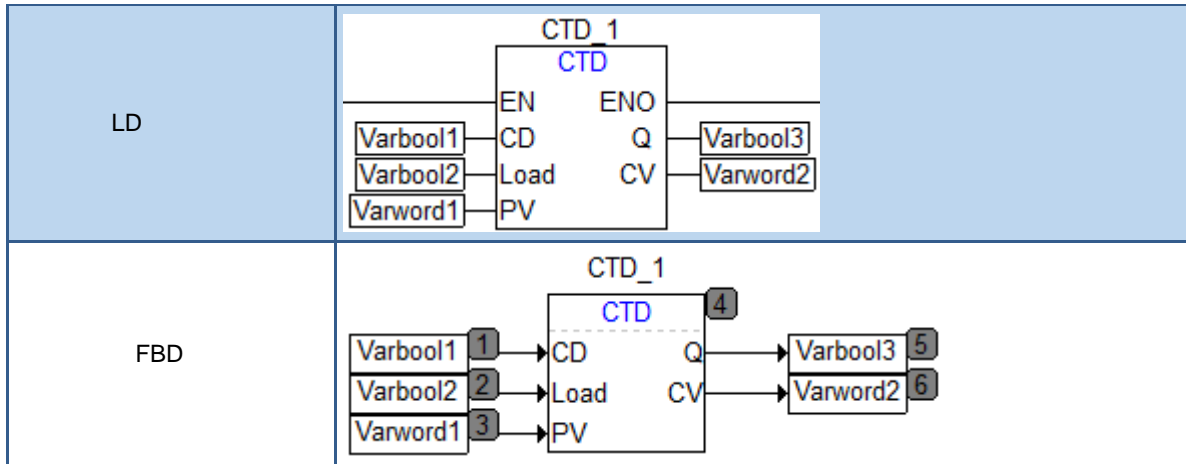
CD	BOOL		0
Load	BOOL	Load TRUE CV PV	0
PV	WORD		0
Q	BOOL	CV TRUE 0 Q	1
CV	WORD		0

2.

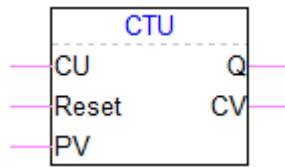
CTD	CD	CD	CD	Load	Q
BOOL	PV	CV	WORD		
Load	TRUE	CV	PV		
Q	CD	FALSE	TRUE	CV	1
	TRUE			CV	0

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	CTD_1		CTD		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Varbool2		BOOL	FALSE	G区
0004	Varword1		WORD	0	G区
0005	Varbool3		BOOL	FALSE	G区
0006	Varword2		WORD	0	G区



### 2.2.9.2 CTU

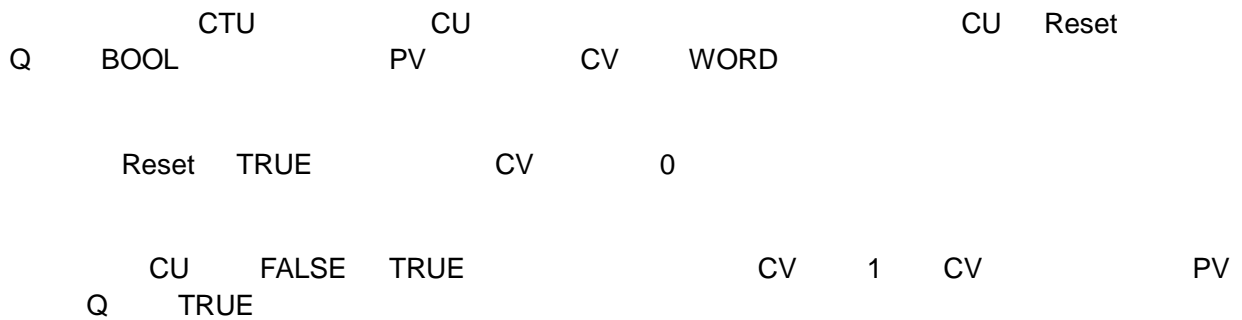


84

1.

CU	BOOL				0
Reset	BOOL	Reset	TRUE	CV	0
		0			
PV	WORD				0
Q	BOOL	CV	TRUE	PV	Q
					0
CV	WORD				0

2.



3.

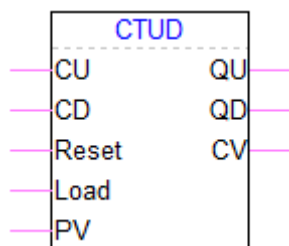


序号	变量名	变量说明	变量类型	初始值	区域
0001	CTU_1		CTU		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Varbool2		BOOL	FALSE	G区
0004	Varword1		WORD	0	G区
0005	Varbool3		BOOL	FALSE	G区
0006	Varword2		WORD	0	G区

LD	
FBD	

### 2.2.9.3 CTUD /

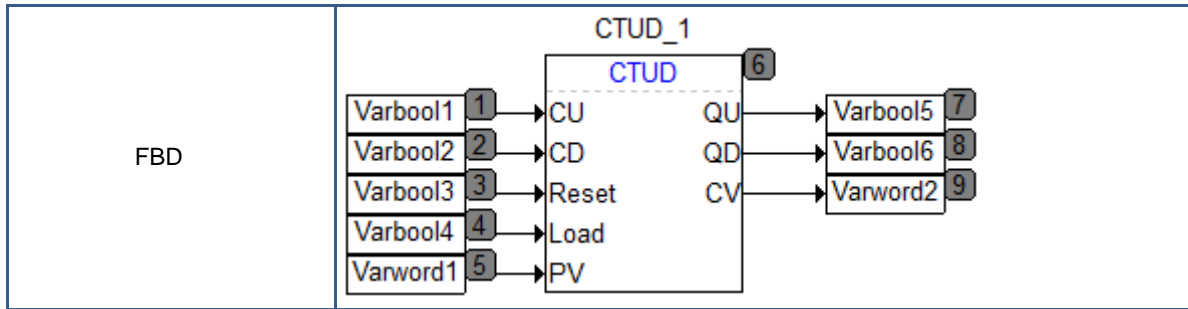


85 /

1.

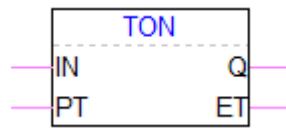
变量名	变量类型	变量说明	初始值
CU	BOOL		0
CD	BOOL		0
Reset	BOOL	Reset TRUE 0	CV 0
Load	BOOL	Load TRUE PV Reset	CV TRUE 0
PV	WORD	CV=0	0





## 2.2.10

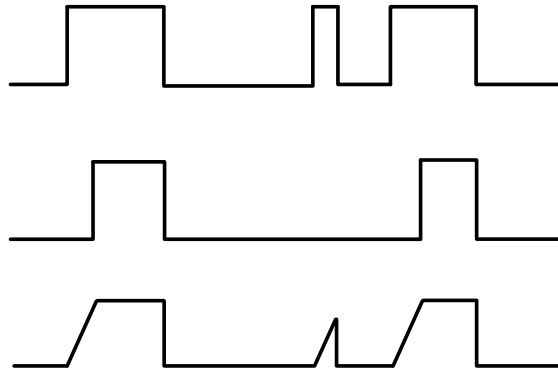
### 2.2.10.1 TON



86

1.

IN	BOOL		0
PT	TIME		T#0MS
Q	BOOL		0



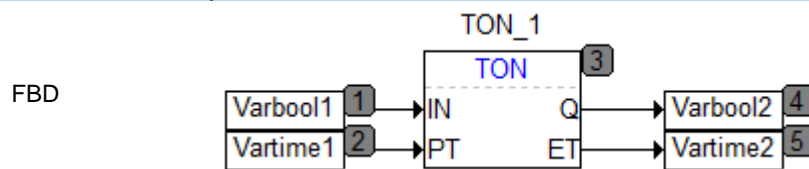
## 87 TON

## 3. 87

序号	变量名	变量说明	变量类型	初始值	区域
0001	TON_1		TON		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Vartime1		TIME	T#0MS	G区
0004	Varbool2		BOOL	FALSE	G区
0005	Vartime2		TIME	T#0MS	G区

LD	
----	--



IN	BOOL		0
PT	TIME		T#0MS
Reset	BOOL		FALSE
Q	BOOL		0
ET	TIME		T#0MS

LD EN EN TRUE FALSE EN TRUE  
 EN FALSE

2.

IN TRUE ET ET PT IN TRUE ET  
 IN FALSE ET Q RESET 1  
 FALSE ET PT

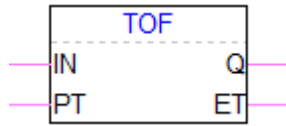
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	TONR_1		TONR		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Vartime1		TIME	T#0MS	G区
0004	Varbool2		BOOL	FALSE	G区
0005	Varbool3		BOOL	FALSE	G区
0006	Vartime2		TIME	T#0MS	G区

LD	
FBD	

### 2.2.10.3 TOF



89

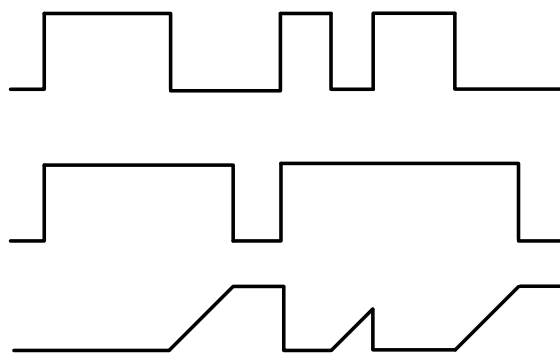
1.

IN	BOOL		0
PT	TIME		T#0MS
Q	BOOL		0
ET	TIME		T#0MS

LD EN TRUE FALSE EN TRUE  
 EN FALSE

2.

TOF IN Q BOOL PT ET TIME  
 ET PT IN TRUE Q TRUE ET >= PT Q FALSE ET = PT  
 PT IN TRUE Q TRUE ET 0

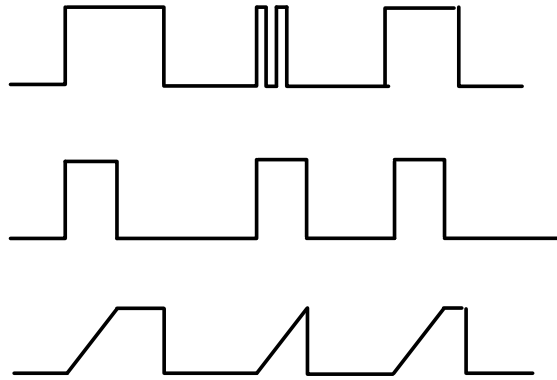


90 TOF

3.







92 TP

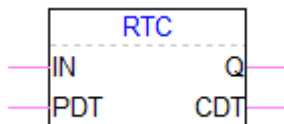
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	TP_1		TP		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Vartime1		TIME	T#0MS	G区
0004	Varbool2		BOOL	FALSE	G区
0005	Vartime2		TIME	T#0MS	G区

LD	
FBD	

2.2.10.5 RTC



93 /

1.



IN	BOOL		0
PDT	DT		DT#1970-01-01-00:00:00
Q	BOOL		0
CDT	DT	IN FALSE 1970-01-01-00-00 00 00 IN TRUE PDT	DT#1970-01-01-00:00:00

LD EN EN TRUE FALSE EN TRUE  
EN FALSE

2.

CDT PDT RTC IN FALSE CDT IN TRUE  
1970-01-01-00:00:00

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	RTC_1		RTC		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Vardt1		DT	DT#1970-01-0...	G区
0004	Varbool2		BOOL	FALSE	G区
0005	Vardt2		DT	DT#1970-01-0...	G区

LD	
FBD	

## 2.3

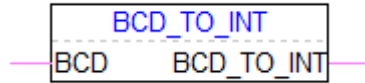
### 2.3.1 BCD

BCD 0 99 4 4-7  
 0-3 BCD 16 BCD 0-99 16  
 0-FF  
 BCD 4 BCD  
 59 BCD 0101 1001 2#111011 51 BCD 5  
 0101 1 0001 51 BCD 0101 0001

#### 2.3.1.1 BCD\_TO\_INT BCD

1.

BCD BCD\_TO\_INT BYTE INT  
 BCD



94 BCD\_TO\_INT

2.

BCD	BYTE	BCD	0
-----	------	-----	---

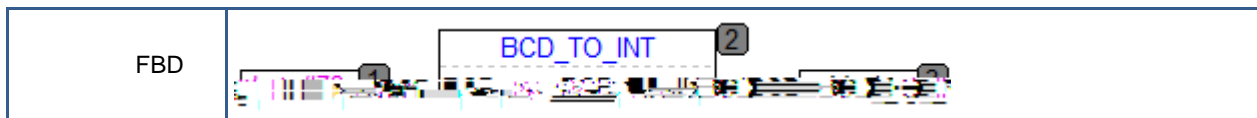
BCD_TO_INT	INT	INT	BCD	-1	0
------------	-----	-----	-----	----	---

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		INT	0	G区

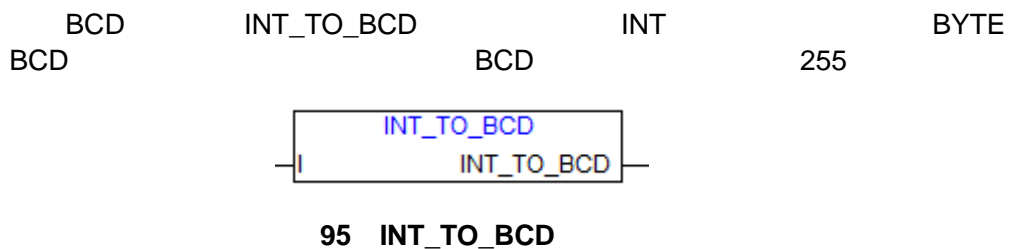
  

LD



### 2.3.1.2 INT\_TO\_BCD BCD

1.



2.

I	INT	49	49	0
INT_TO_BCD	BYTE	BCD		0

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	Var1		BYTE	0	G区

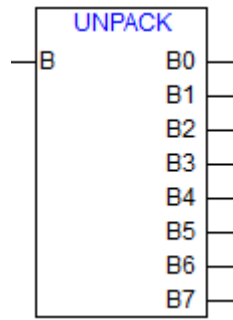
LD	
FBD	

## 2.3.2

### 2.3.2.1 UNPACK

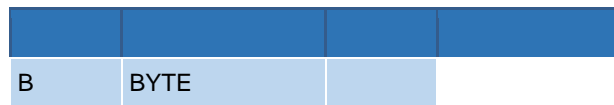
1.

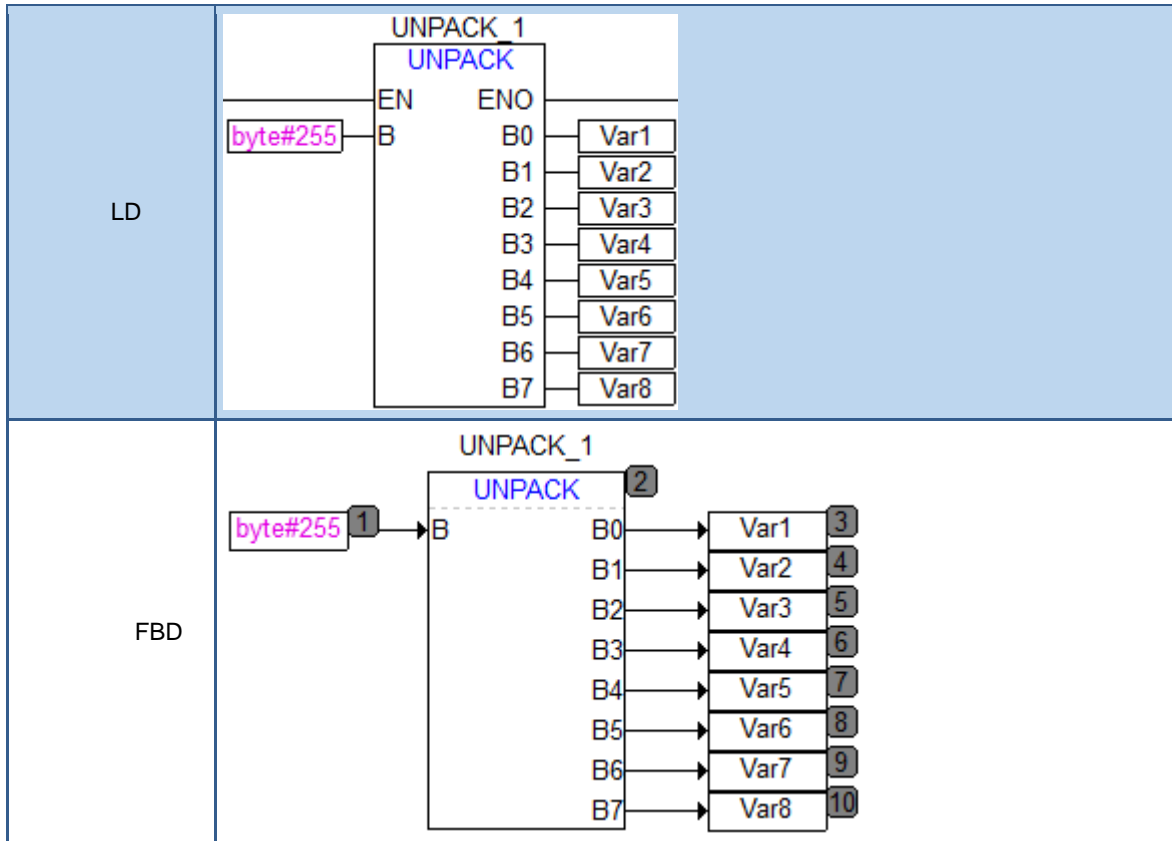




96 UNPACK

2.

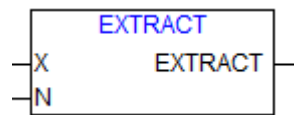




### 2.3.2.2 EXTRACT

1.

X N



97 EXTRACT

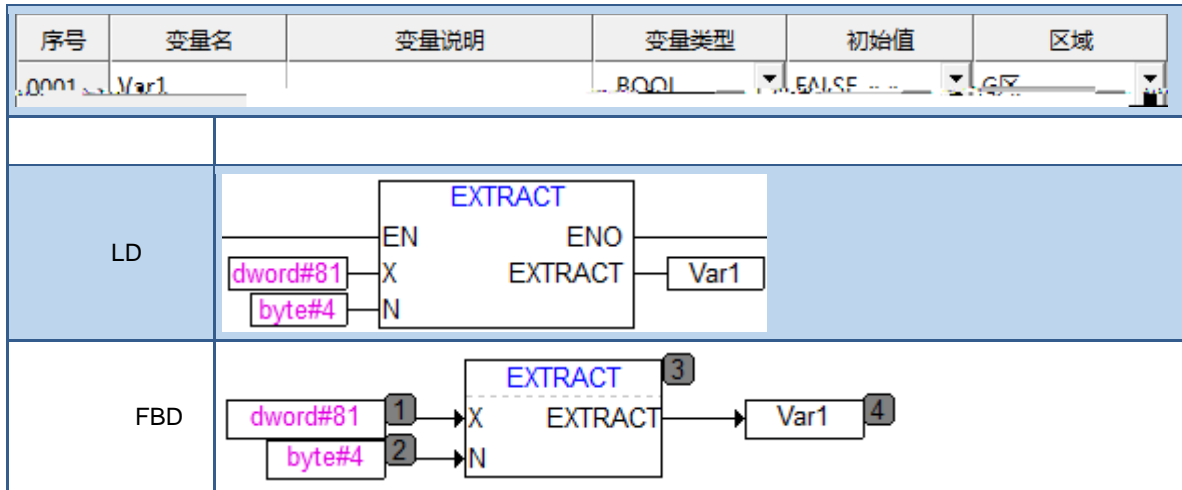
2.

X	DWORD		0
N	BYTE		0

EXTRACT	BOOL		0

3.



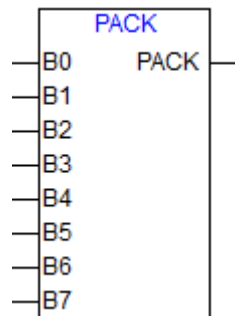


### 2.3.2.3 PACK

1.

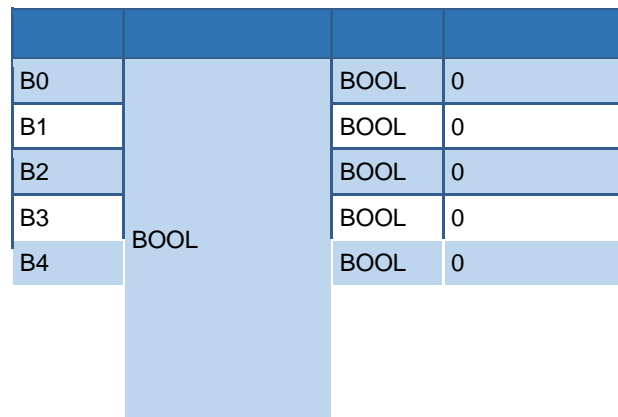
B0-B7      0      7      BYTE

UNPACK

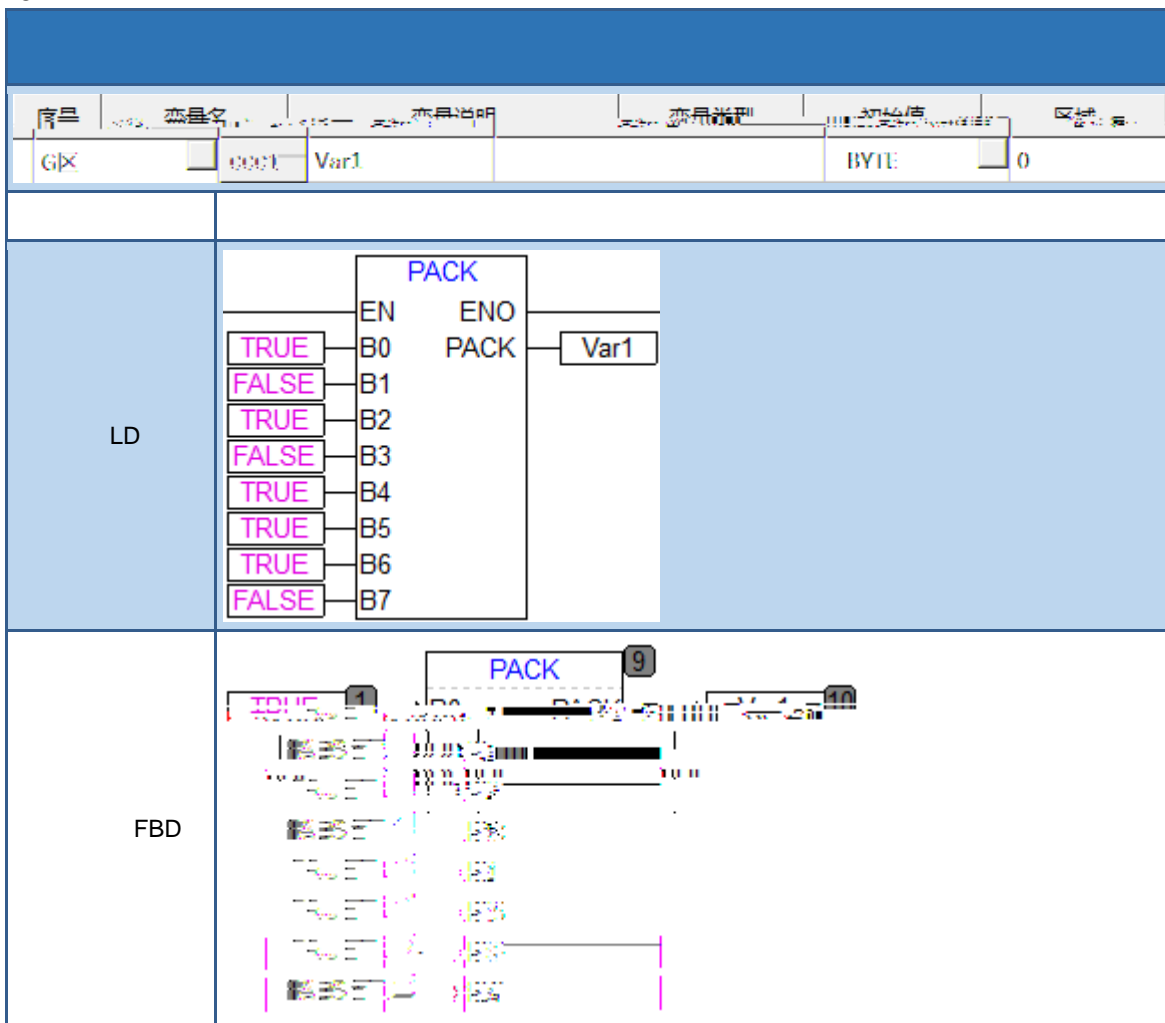


98 PACK

2.



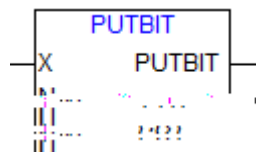
3.



### 2.3.2.4 PUTBIT

1.

PUTBIT                      X                      N                      B



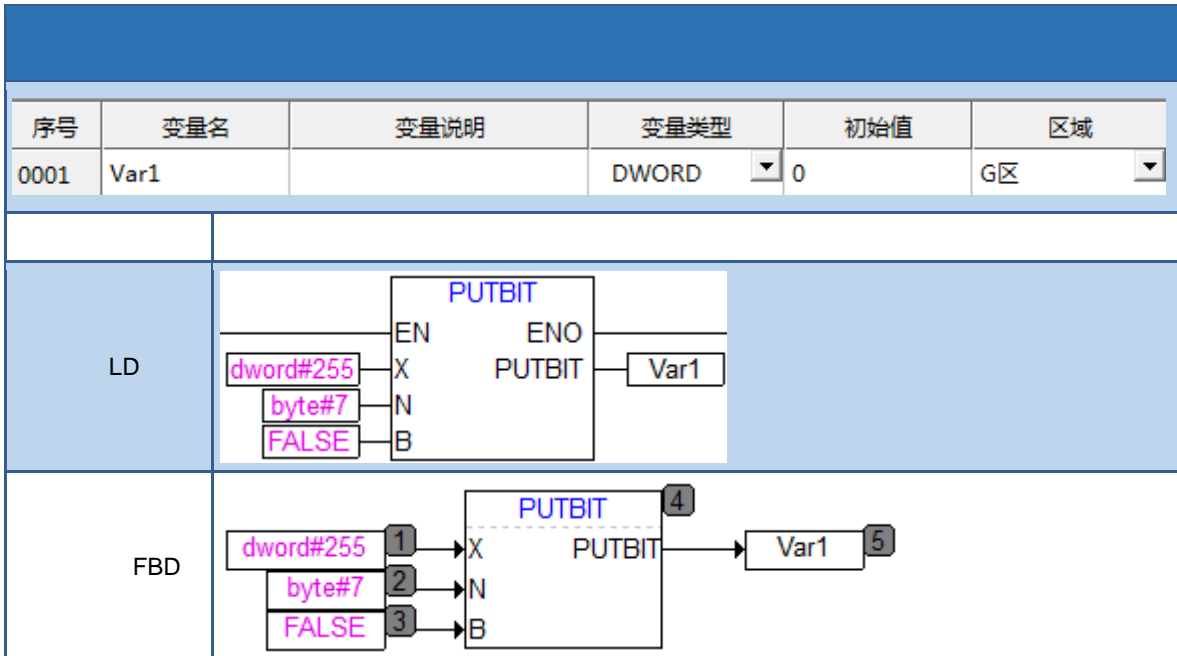
99 PUTBIT

2.

X	DWORD		0
N	BYTE		0
B	BOOL		FALSE

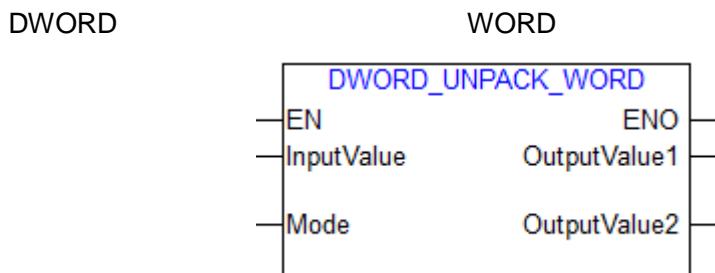
PutBit	DWORD		
--------	-------	--	--

3.



### 2.3.2.5 DWORD\_UNPACK\_WORD DWORD WORD

1.



100 DWORD\_UNPACK\_WORD

2.

InputValue	DWORD			
Mode	BOOL	FALSE	16	16
		TRUE	16	16

OutputValue1	WORD	Mode	FALSE	16
--------------	------	------	-------	----

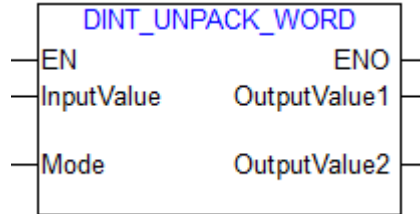
		Mode	TRUE	16
OutputValue2	WORD	Mode	FALSE	16
		Mode	TRUE	16

**2.3.2.6 DINT\_UNPACK\_WORD DINT WORD**

1.

DINT

WORD



**101 DINT\_UNPACK\_WORD**

2.

InputValue	DINT			
Mode	BOOL	FALSE	16	16
		TRUE	16	16

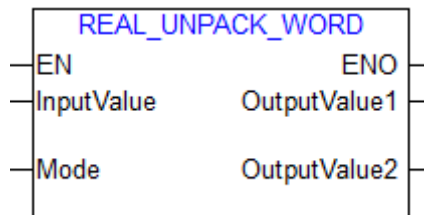
OutputValue1	WORD	Mode	FALSE	16
		Mode	TRUE	16
OutputValue2	WORD	Mode	FALSE	16
		Mode	TRUE	16

**2.3.2.7 REAL\_UNPACK\_WORD REAL WORD**

1.

REAL

WORD



**102 REAL\_UNPACK\_WORD**

2.

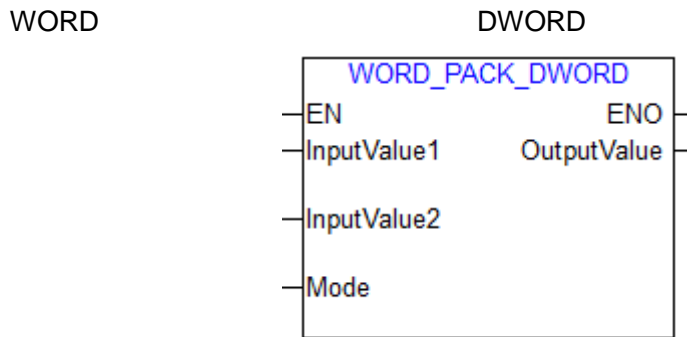
--	--	--	--	--

InputValue	REAL			
Mode	BOOL	FALSE	16	16
		TRUE	16	16

OutputValue1	WORD	Mode	FALSE	16
		Mode	TRUE	16
OutputValue2	WORD	Mode	FALSE	16
		Mode	TRUE	16

**2.3.2.8 WORD\_PACK\_DWORD WORD DWORD**

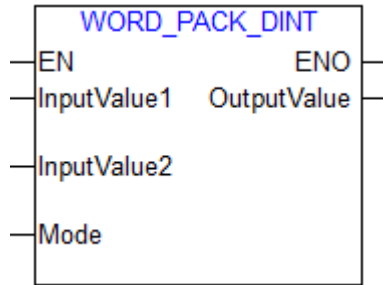
1.



**103 WORD\_PACK\_DWORD**

2.

InputValue1	WORD	1		
InputValue2	WORD	2		
Mode	BOOL	FALSE	InputValue1	16
		TRUE	InputValue1	16
			InputValue2	16
			InputValue2	16



**104 WORD\_PACK\_DINT**

2.

InputValue1	WORD	1
InputValue2	WORD	2
Mode	BOOL	FALSE InputValue1 16 InputValue2 16 TRUE InputValue1 16 InputValue2 16

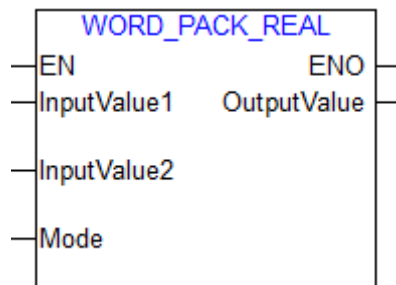
OutputValue	DINT	Mode
-------------	------	------

**2.3.2.10 WORD\_PACK\_REAL WORD REAL**

1.

WORD

REAL



**105 WORD\_PACK\_REAL**

2.

InputValue1	WORD	1
InputValue2	WORD	2
Mode	BOOL	FALSE InputValue1 16 InputValue2 16 TRUE InputValue1 16 InputValue2 16

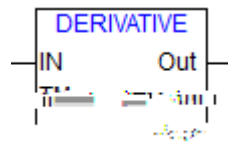
OutputValue	REAL	Mode
-------------	------	------

### 2.3.3

#### 2.3.3.1 DERIVATIVE

1.

DERIVATIVE



106 DERIVATIVE

2.

$$OUT = \frac{3 * [IN(k) - IN(k - 3)] + IN(k - 1) - IN(k - 2)}{3 * TM(k - 2) + 4 * TM(k - 1) + 3 * TM(k)} * 1000$$

k-3 k-2 k-1 k

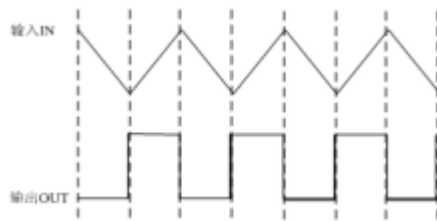
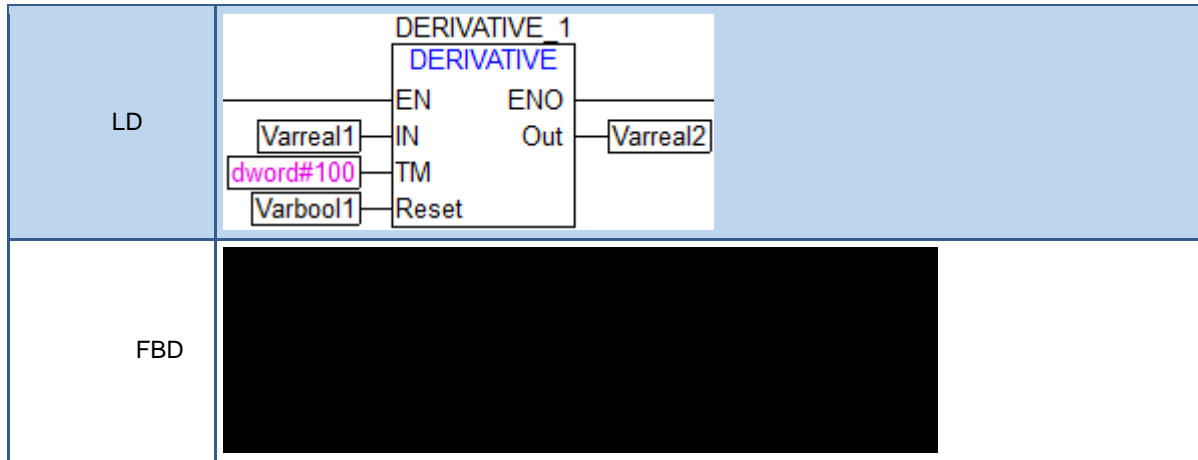
3.

IN	REAL		0
TM	DWORD		0
Reset	BOOL		FALSE

OUT	REAL		0
-----	------	--	---

4.

序号	变量名	变量说明	变量类型	初始值	区域
0001	DERIVATIVE_1		DERIVATIVE		G区
0002	Varreal1		REAL	0	G区
0003	Varbool1		BOOL	FALSE	G区
0004	Varreal2		REAL	0	G区

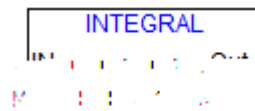


107

### 2.3.3.2 INTEGRAL

1.

INTEGRAL



108 INTEGRAL

2.

$$OUT(k) = OUT(k-1) + TM * IN(k)$$

k-1 k

3.

IN	REAL	0	FALSE
TM	DWORD	0	TRUE
Reset	BOOL	FALSE	TRUE

OUT	REAL	0	TRUE
-----	------	---	------

OverFlow	BOOL		FALSE	TRUE
----------	------	--	-------	------

4.

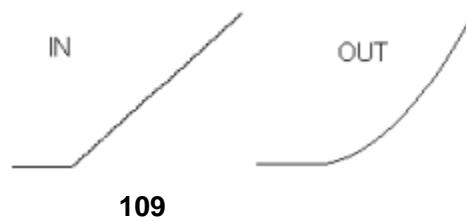
序号	变量名	变量说明	变量类型	初始值	区域
0001	INTEGRAL_1		INTEGRAL		G区
0002	Varreal1		REAL	0	G区
0003	Varbool1		BOOL	FALSE	G区
0004	Varreal2		REAL	0	G区
0005	Varbool2		BOOL	FALSE	G区

LD

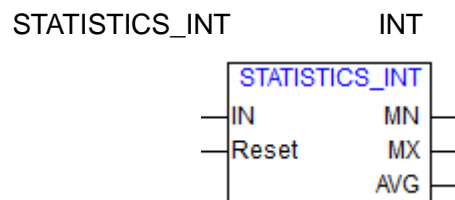
  

FBD



### 2.3.3.3 STATISTICS\_INT

1.



110 STATISTICS\_INT

2.



IN	INT		0	FALSE
Reset	BOOL		FALSE	TRUE

MN	INT		32,767	TRUE
MX	INT		-32,768	TRUE
AVG	INT		0	FALSE

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	STATISTICS_INT_1		STATISTICS_INT		GE
0002	Varint1		INT	0	GE
0003	Varbool1		BOOL	FALSE	GE
0004	Varint2		INT	0	GE
0005	Varint3		INT	0	GE
0006	Varint4		INT	0	GE

LD

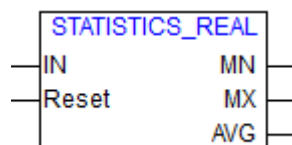
  

FBD

### 2.3.3.4 STATISTICS\_REAL

1.

REAL



111 STATISTICS\_REAL

2.

IN	REAL		0	FALSE
Reset	BOOL		FALSE	TRUE

MN	REAL		3.4E+38	TRUE
MX	REAL		3.4E+38	TRUE
AVG	REAL		0	TRUE

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	STATISTICS_REAL_1		STATISTICS_REAL		G区
0002	Varreal1		REAL	0	G区
0003	Varbool1		BOOL	FALSE	G区
0004	Varreal2		REAL	0	G区
0005	Varreal3		REAL	0	G区
0006	Varreal4		REAL	0	G区

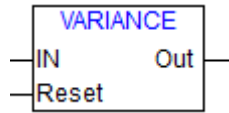
  

LD	
FBD	

### 2.3.3.5 VARIANCE

1.

VARIANCE



**112 VARIANCE**

2.

IN	REAL		0	FALSE
Reset	BOOL		FALSE	TRUE

Out	REAL		0	FALSE
-----	------	--	---	-------

Z	DWORD	0	TRUE
A	LREAL	0	TRUE
B	LREAL	0	TRUE

3.

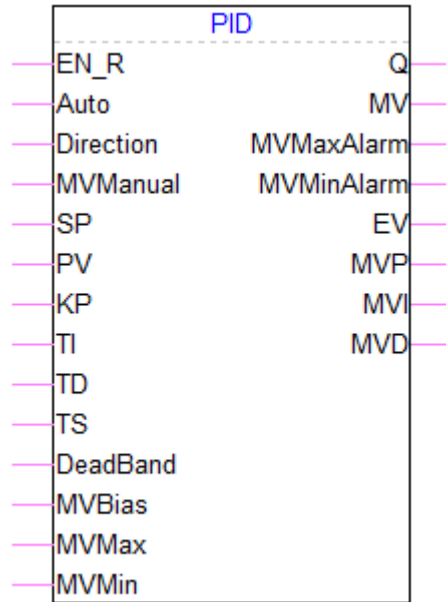
VARIANCE						
VARIANCE			GEX	0001	VARIANCE_1	
REAL	0		GEX	0002	Varreal1	
BOOL	FALSE		GEX	0003	Varbool1	
REAL	0		GEX	0004	Varreal2	
LD						

## 2.3.4

### 2.3.4.1 PID

1.

PID



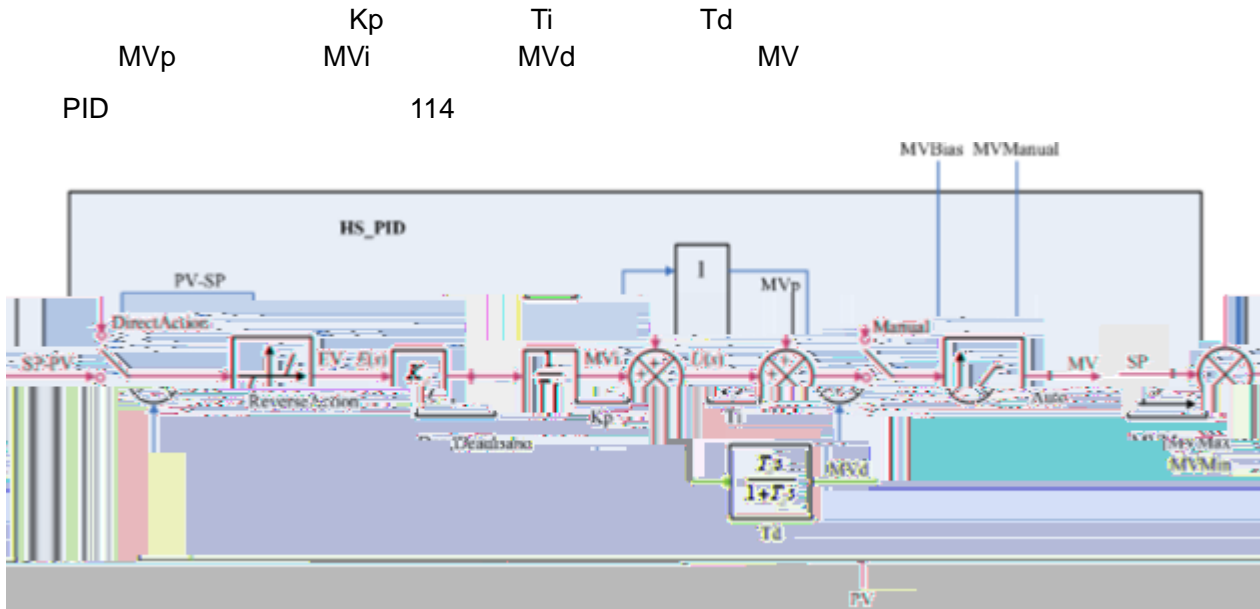
113 PID

2.

EN_R			FALSE TRUE			
Auto	BOOL		FALSE TRUE		FALSE TRUE	
Direction	BOOL		FALSE	EV=SP-PV		

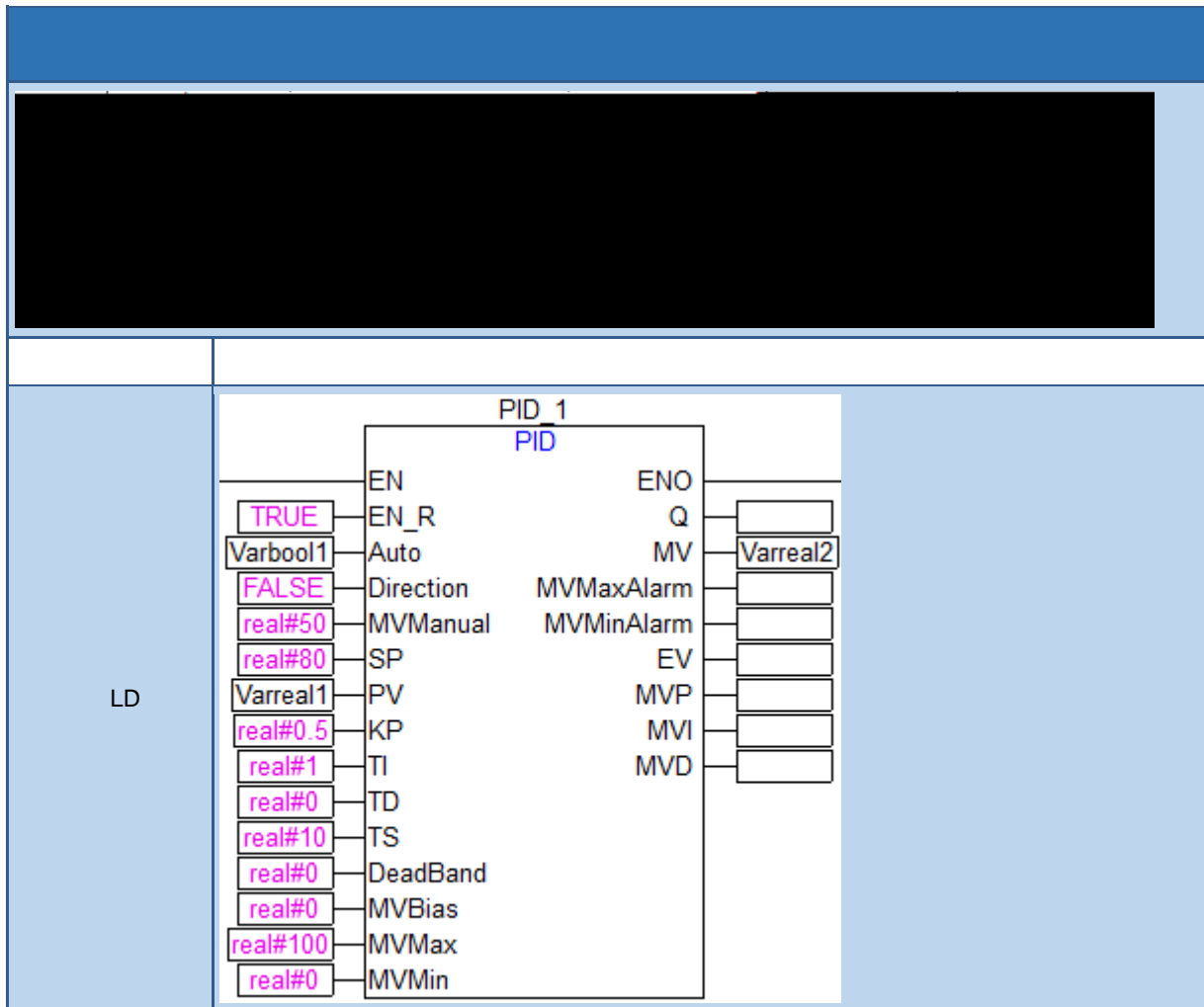
TS	REAL	S	Ts>0	0.1	TRUE
DeadBand	REAL		1 DeadBand>=0 2 DeadBand=0 3 0	0	TRUE
MVBias	REAL			0.0	TRUE
MVMax	REAL			100.0	TRUE
MVMin	REAL			-100.0	TRUE

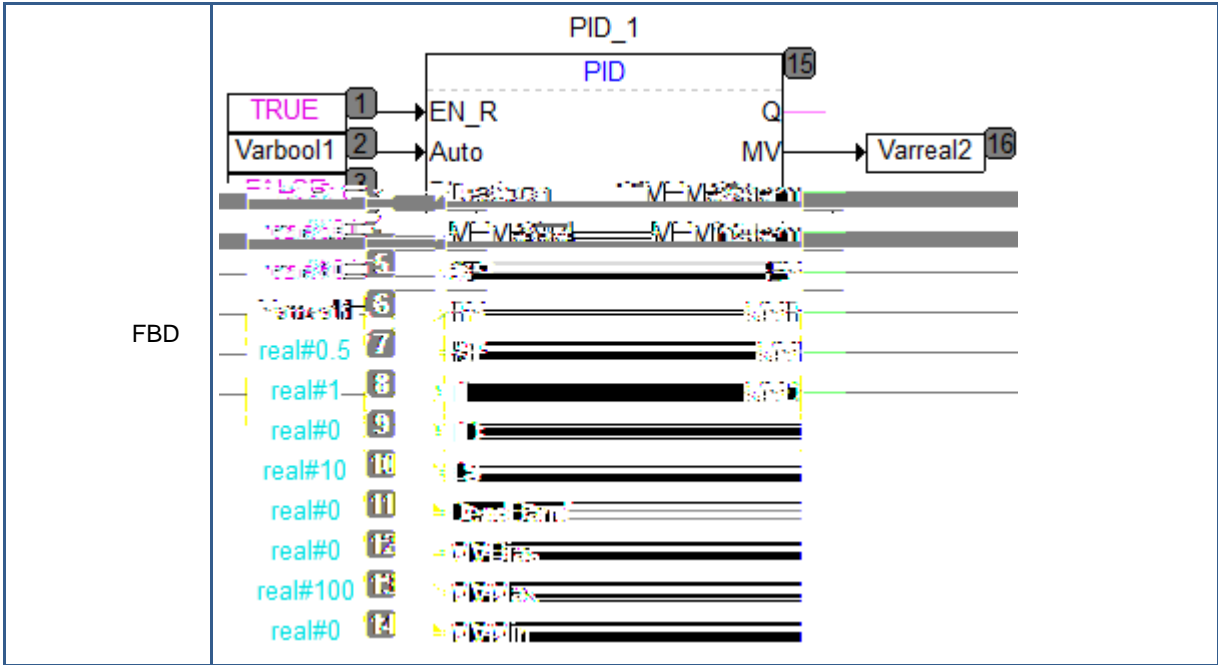
Q	BOOL		FALSE TRUE	FALSE	FALSE
MV	REAL			0	TRUE
MVMaxAlarm	BOOL		FALSE TRUE	FALSE	FALSE
MVMinAlarm	BOOL		FALSE TRUE	FALSE	FALSE
EV	REAL		EV	0	FALSE
MVP	REAL			0	FALSE
MVI	REAL			0	



114 PID

4.



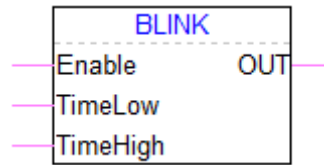


### 2.3.5

#### 2.3.5.1 BLINK

1.

BLINK

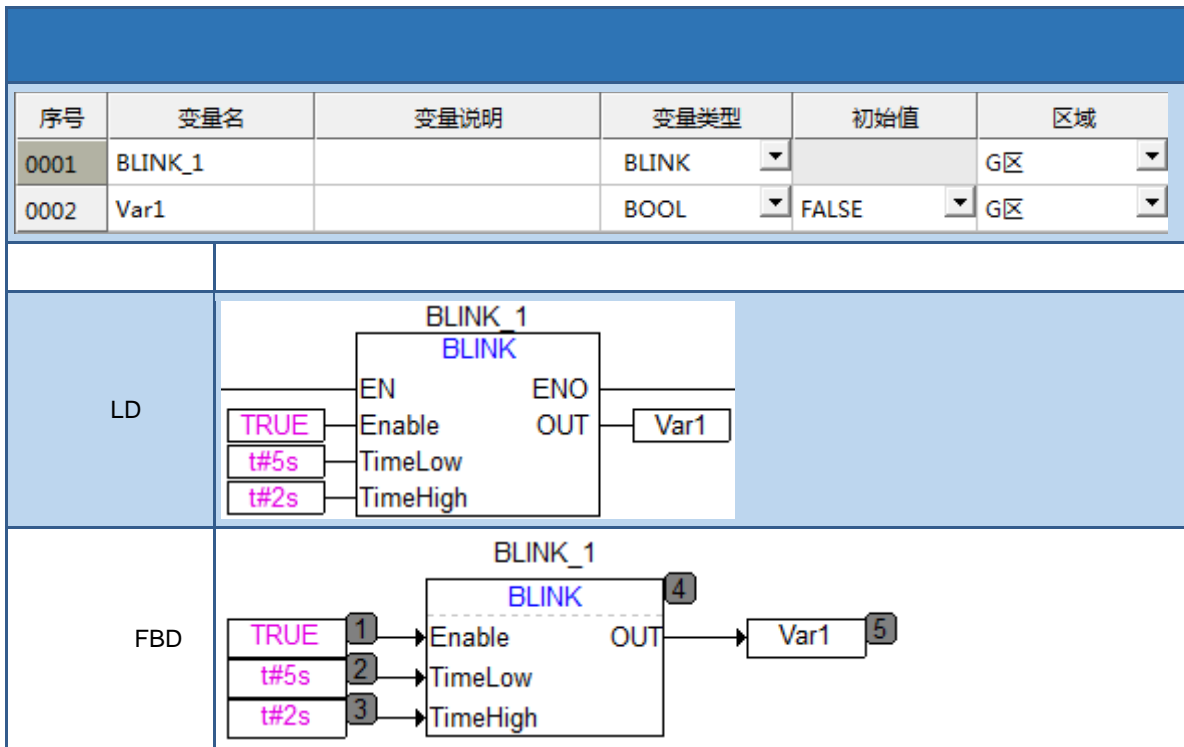


115 BLINK

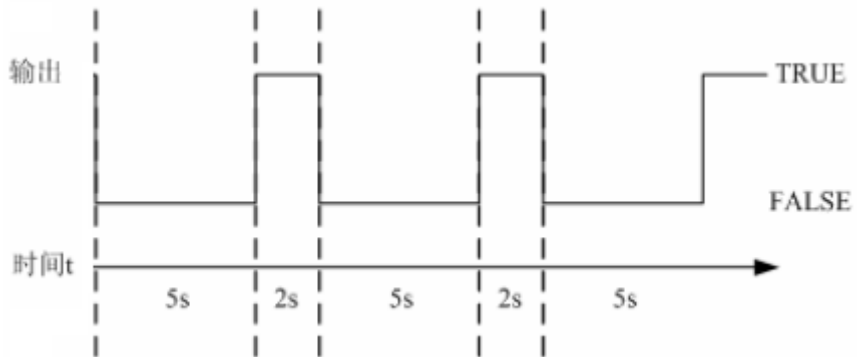
2.

Enable		BOOL	FALSE	TRUE
TimeLow		TIME	T#0S	TRUE
TimeHigh		TIME	T#0S	TRUE

3.



Out 116

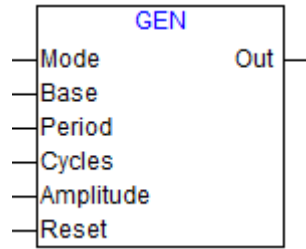


116

2.3.5.2 GEN

1.

GEN



117 GEN

2.

Mode	BYTE	MODE		0	TRUE
		0	TRIANGLE		
		1	TRIANGLE_POS		
		2	SAWTOOTH_RISE		
		3	SAWTOOTH_FALL		
		4	RECTANGLE		
		5	SINUS		
		6	COSINU		
Base	BOOL	BASE TRUE BASE FALSE		FALSE	TRUE
Period	TIME	Base		T#0MS	TRUE
Cycles	DWORD	Base		0	TRUE
Amplitude	INT			0	TRUE
Reset	BOOL	RESET=TRUE 0		FALSE	TRUE
Out		INT		0	TRUE

LD

EN

EN FALSE TRUE FALSE EN TRUE  
 EN FALSE

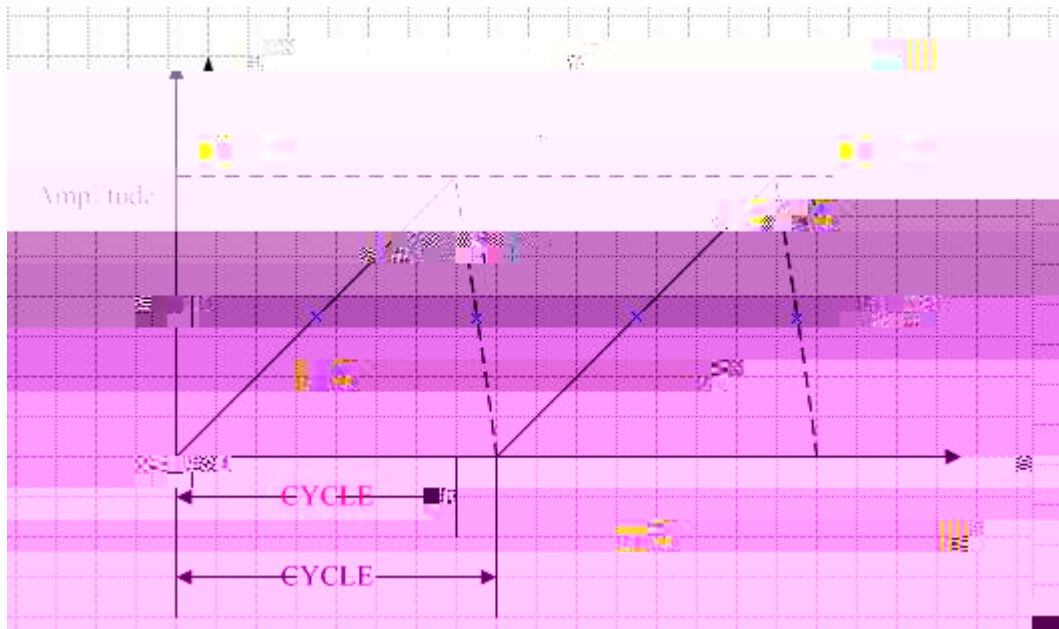
3.

RESET=1

BASE=0 CYCLES PERIOD AMPLITUDE MODE BASE=1

4.

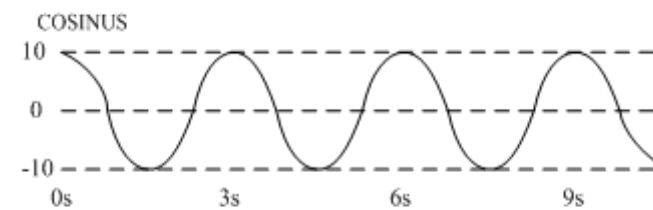
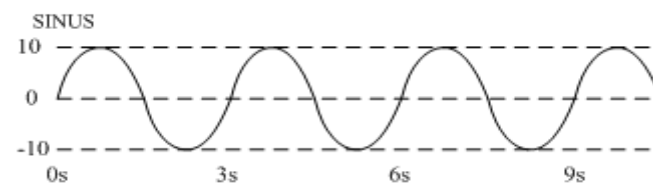
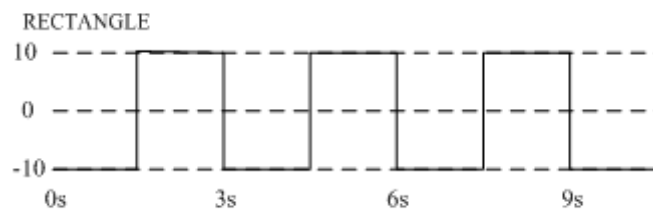
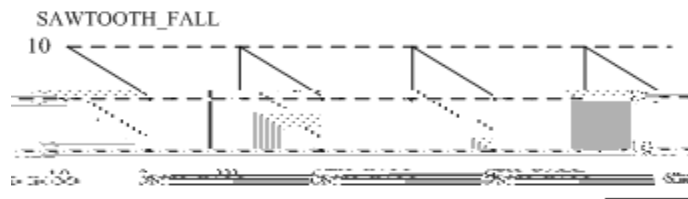
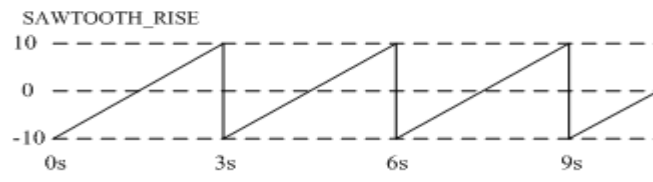
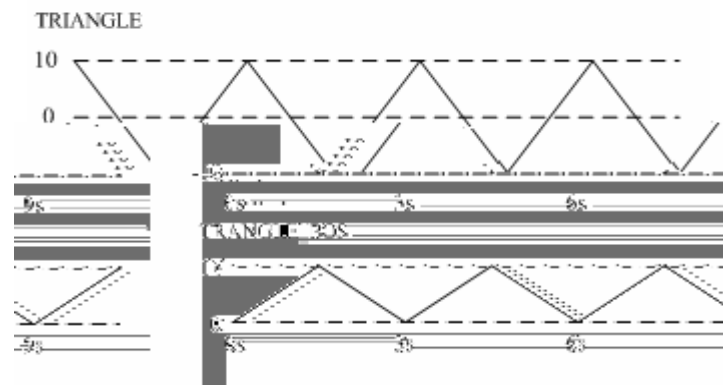
BASE=0 SAWTOOTH\_RISE SAWTOOTH\_FALL CYCLE  
 118

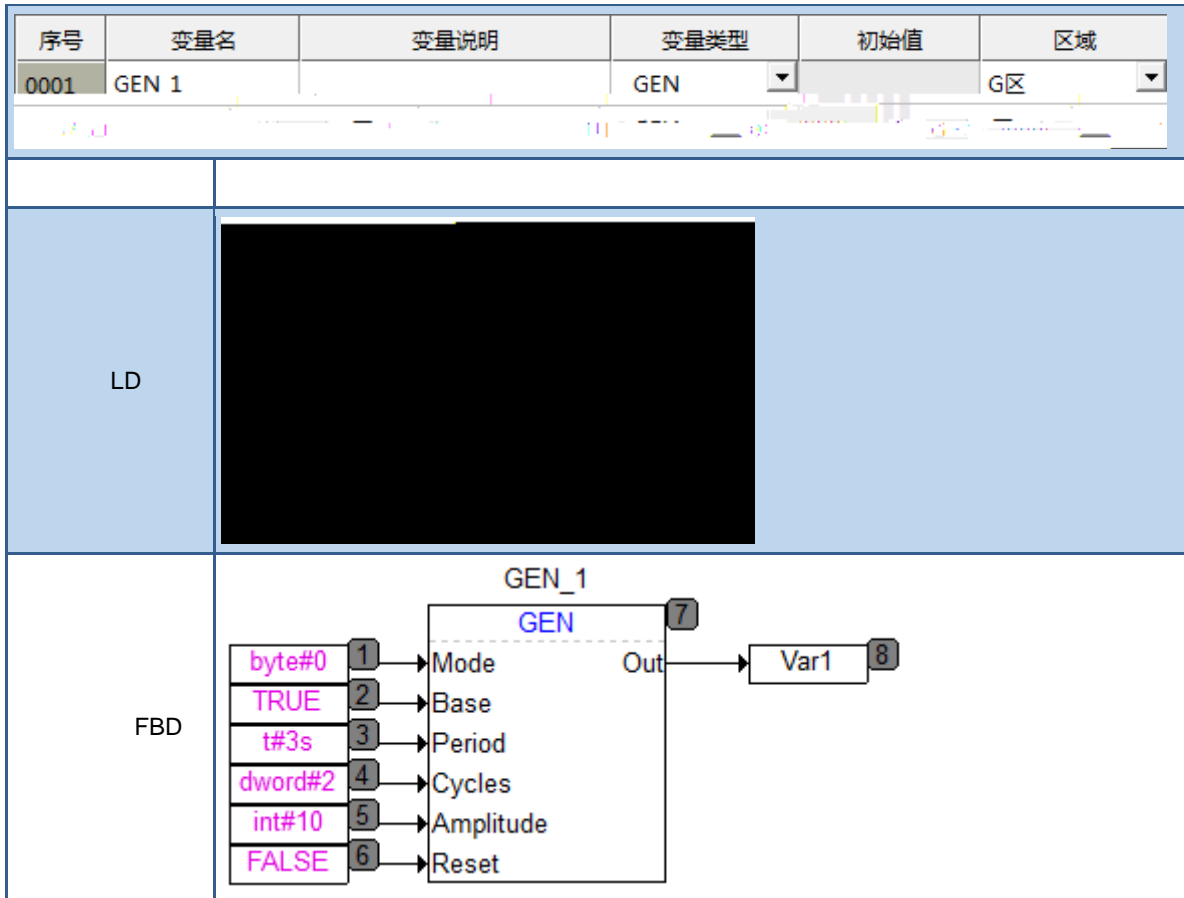


118

BASE=1 SINUS COSINUS IEC PERIOD  
 0.1 0.1

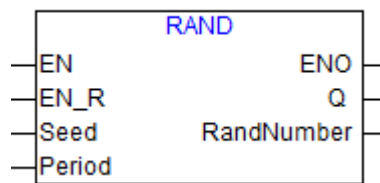
Mode





### 2.3.5.3 RAND

1.



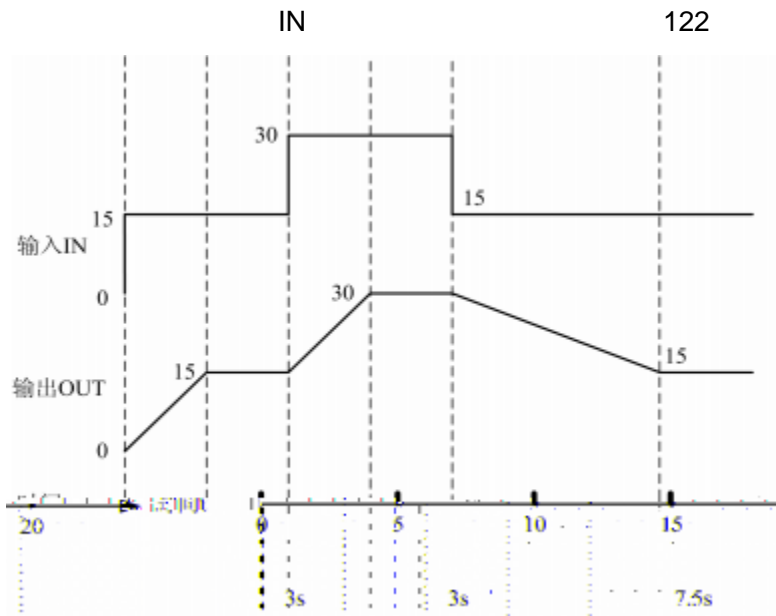
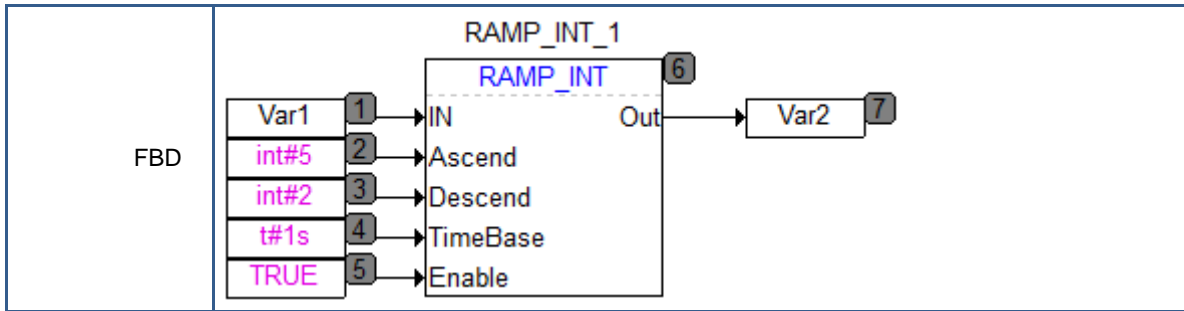
120 RAND

2.

变量名	数据类型	初始值	区域
EN_R	BOOL	0 RandNumber	FALSE FALSE
Seed	WORD	0-65,535	0 FALSE
Period	DWORD	0	0 FALSE







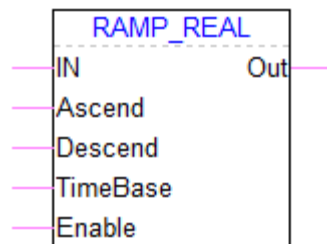
122 IN OUT

### 2.3.6.2 RAMP\_REAL

1.

RAMP\_REAL

OUT	IN	TIMEBASE	t#0s	TIMEBASE	t#0s	ASCEND
DESCEND				ASCEND		DESCEND
TIMEBASE						



123 RAMP\_REAL

2.

IN	REAL		0	FALSE
Ascend	REAL		0	TRUE
DEScend	REAL		0	TRUE
TimeBase	TIME		T#1S	TRUE
Enable	BOOL		TRUE	TRUE

Out	REAL		0	TRUE
-----	------	--	---	------

LD EN  
 EN TRUE FALSE EN TRUE  
 EN FALSE

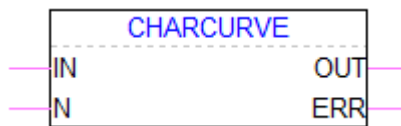
3.

2.3.6.1 RAMP\_INT

2.3.6.3 CHARCURVE

1.

IN INT N 2=<N<=11 INT  
 PX[0..10] PY[0..10] INT OUT ERR  
 PX[0]..PX[N-1] ERR 1 IN<PX[0] ERR=2  
 IN>PX[N-1] ERR=3 PY[0] PY[N-1]  
 N 2 11 ERR=4



124 CHARCURVE

2.

IN	INT		0	FALSE
N	BYTE		0	TRUE

OUT	INT		0	FALSE
ERR	BYTE		0	FALSE

PX	ARRAY[0..10] OF INT	X	0	TRUE
PY	ARRAY[0..10] OF INT	Y	0	TRUE

3.

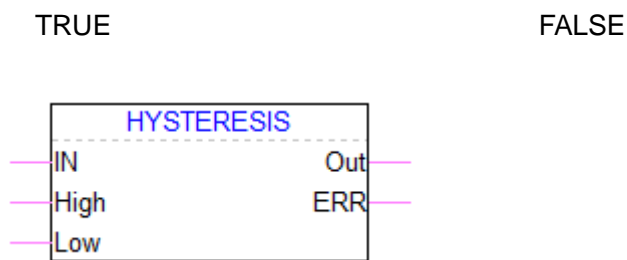
序号	变量名	变量说明	变量类型	初始值	区域
0001	CHARCURVE_1		CHARCURVE		G区
0002	Varint1		INT	0	G区
0003	Varbyte1		BYTE	0	G区

LD	
FBD	

### 2.3.6.4 HYSTERESIS

1.



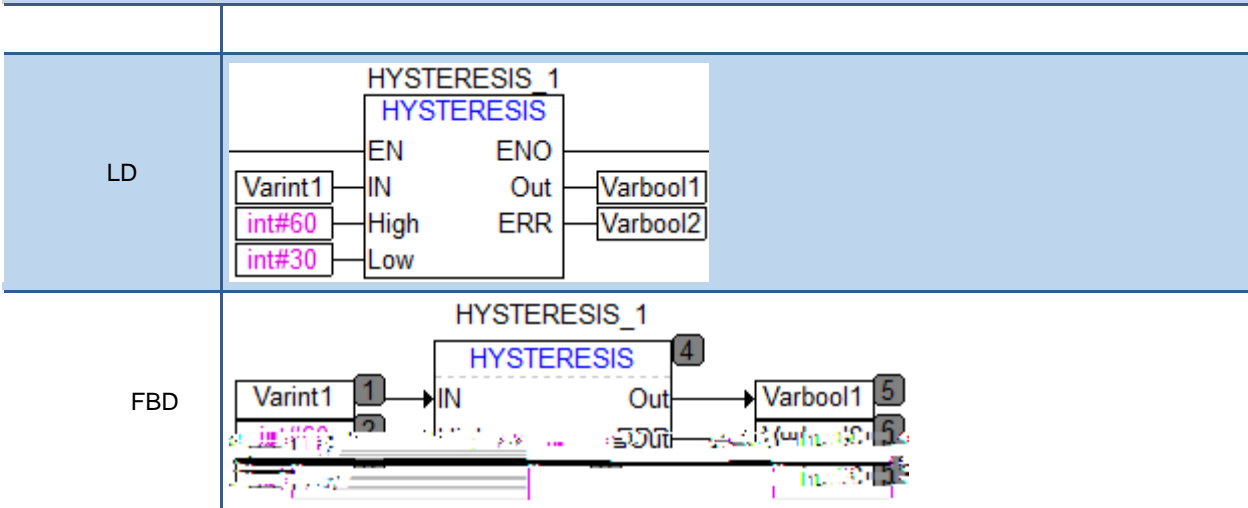
125 HYSTERESIS

2.

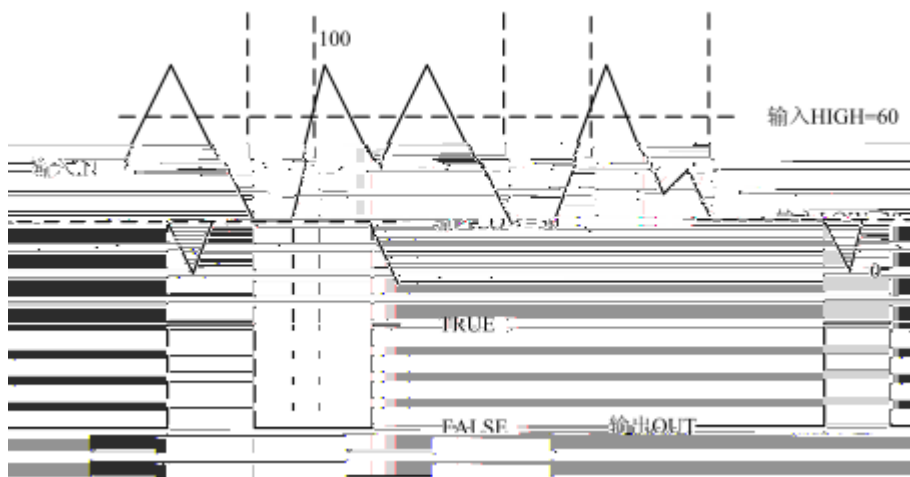
IN	INT		0	FALSE
Low	INT		0	TRUE
High	INT		0	TRUE

Out	BOOL	TRUE	FALSE	TRUE
ERR	BOOL	ERR		FALSE

序号	变量名	变量说明	变量类型	初始值	区域
0001	HYSTERESIS_1		HYSTERESIS		G区
0002	Varint1		INT	0	G区
0003	Varbool1		BOOL	FALSE	G区
0004	Varbool2		BOOL	FALSE	G区



126

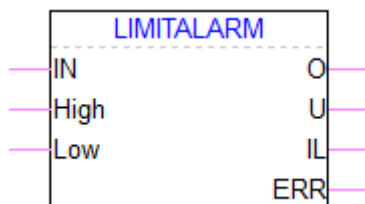


126

### 2.3.6.5 LIMITALARM

1.

U TRUE O TRUE  
IL TRUE



127 LIMITALARM

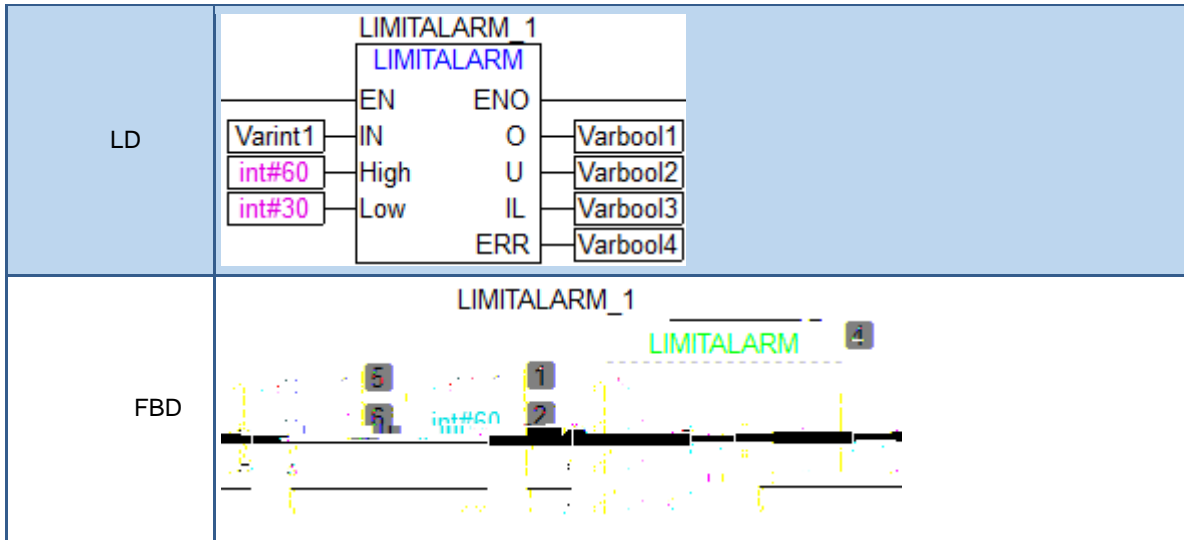
2.

IN		INT	0	FALSE
Low		INT	0	TRUE
High		INT	0	TRUE

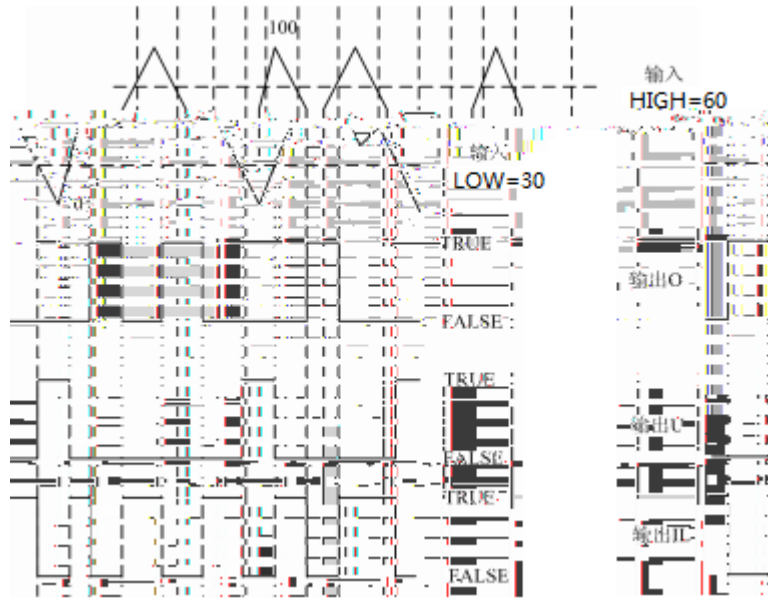
O		BOOL	FALSE	FALSE
U		BOOL	FALSE	FALSE
IL		BOOL	FALSE	FALSE
ERR	ERR	BOOL	FALSE	FALSE

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	LIMITALARM_1		LIMITALARM		G区
0002	Varint1		INT	0	G区
0003	Varbool1		BOOL	FALSE	G区
0004	Varbool2		BOOL	FALSE	G区
0005	Varbool3		BOOL	FALSE	G区
0006	Varbool4		BOOL	FALSE	G区



128



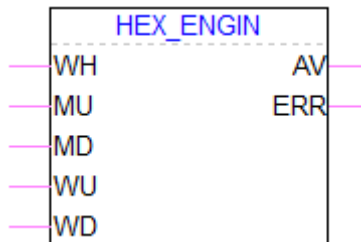
128

### 2.3.6.6 HEX\_ENGIN 16

1.

WH

AV



129 HEX\_ENGIN

2.

变量名	数据类型	初始值	区域
WH	WORD	0	FALSE
MU	REAL	0	TRUE
MD	REAL	0	TRUE
WU	WORD	0	TRUE
WD	WORD	0	TRUE

变量名	数据类型	初始值	区域
AV	REAL	0	FALSE
ERR	BOOL	FALSE	FALSE

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	HEX_ENGIN_1		HEX_ENGIN		G区
0002	Varword1		WORD	0	G区
0003	Varreal1		REAL	0	G区
0004	Varbool1		BOOL	FALSE	G区

HEX ENGIN 1  
HEX\_ENGIN

The diagram shows a Ladder Logic (LD) instruction. On the left, there are five variable boxes: Varword1, real#1.6, real#0, word#65535, and word#0. On the right, there are two variable boxes: Varreal1 and Varbool1. These are connected to the inputs and outputs of the HEX\_ENGIN block. Specifically, Varword1 connects to WH, real#1.6 to MU, real#0 to MD, word#65535 to WU, word#0 to WD, Varreal1 to AV, and Varbool1 to ERR.



序号	变量名	变量说明	变量类型	初始值	区域
	G区	0001	ENGIN_HEX_1		ENGIN_HEX
0	G区	0002	Varreal1		REAL
0	G区	0003	Varword1		WORD
FALSE	G区	0004	Varbool1		BOOL

LD

FBD

50 Hz                      0 Hz                      EN                      0~65,535                      Varreal1                      Varreal1  
 Varword1                      32,768                                                                                     25 Hz

## 2.4

### 2.4.1

#### 2.4.1.1 GetTickCnt

1.

0~2<sup>32</sup>

0

2.

	GetTickCnt	DWORD	

### 2.4.1.2 TimeDelay

1.

IEC

2.

	uiTimeDelay	DWORD	0
	TimeDelay	DINT	0 -1

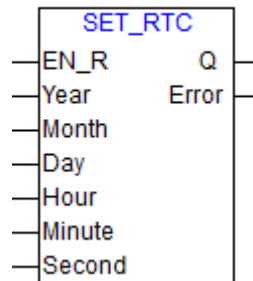
### 2.4.1.3 SET\_RTC

1.

PLC

RTC

RTC



131 SET\_RTC

2.

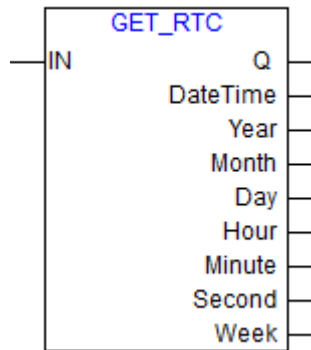
EN_R	BOOL		FALSE TRUE	FALSE
Year	WORD		1971~2036	2011
Month	BYTE		1~12	1
Day	BYTE		1~31	1
Hour	BYTE		0~23	0
Minute	BYTE		0~59	0
Second	BYTE		0~59	0

Q	BOOL		0 1	FALSE
Error	BYTE		=0      RTC 0      RTC =1 =2	0

			=3		
			=4		
			=5		
			=6		
			=200		

3.





132 GET\_RTC

2.

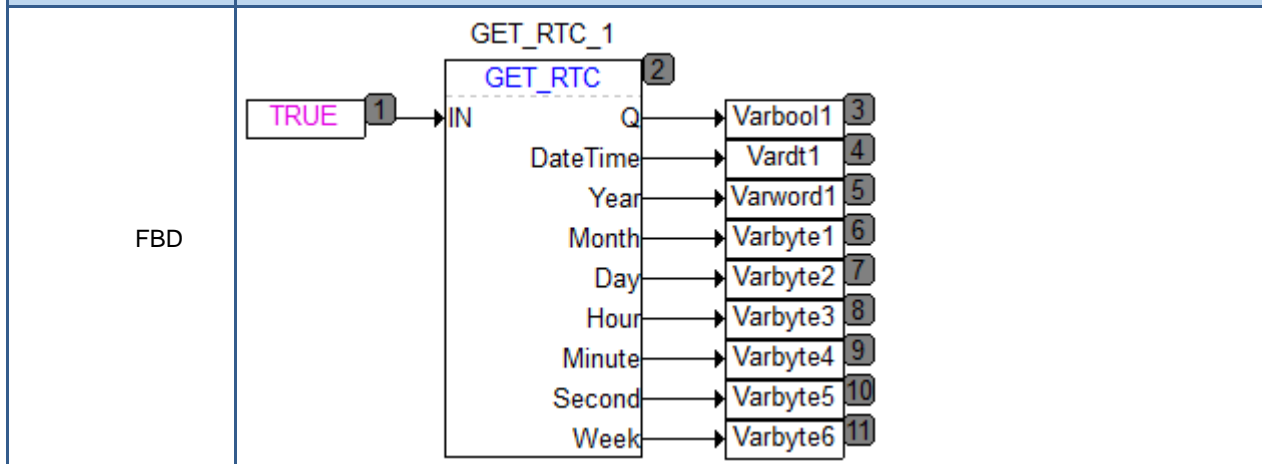
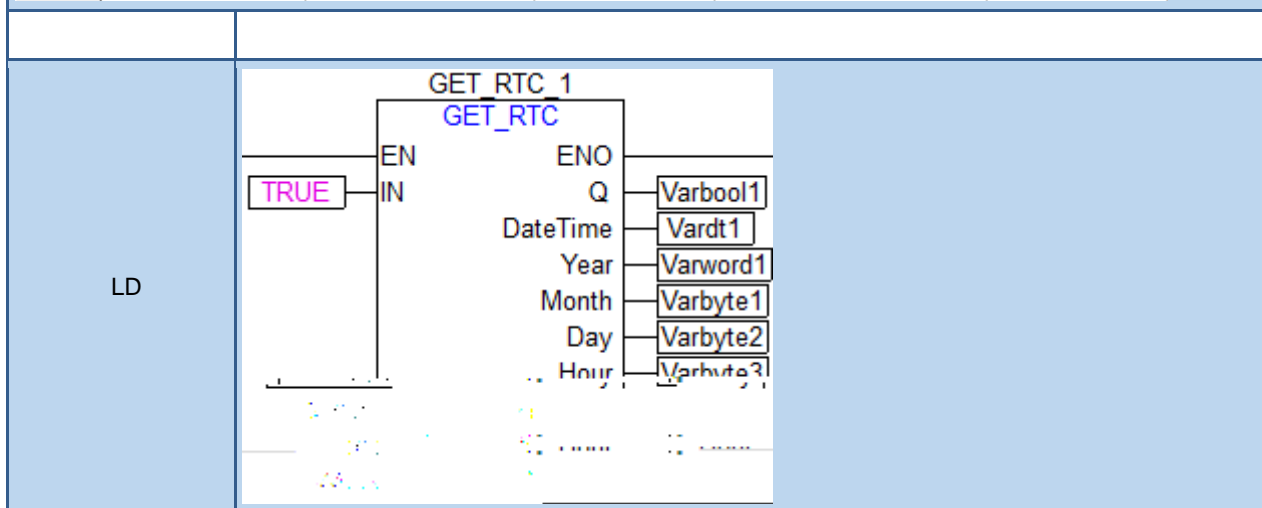
IN	BOOL		0 1	0
----	------	--	--------	---

Q	BOOL		0 1	FALSE
DateTime	DT	/	DT#1971-01-01-00:00:00~DT#2036-12-31-23:59:59	DT#1970-01-01-00:00:00
Year	WORD		1971-2036	0
Month	BYTE		1-12	0
Day	BYTE		1-31	0
Hour	BYTE		0-23	0
Minute	BYTE		0-59	0
Second	BYTE		0-59	0
Week	BYTE		1-7	0

3.



序号	变量名	变量说明	变量类型	初始值	区域
0001	GET_RTC_1		GET_RTC		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Vardt1		DT	DT#1970-01-01-00:00:00	G区
0004	Varword1		WORD	0	G区
0005	Varbyte1		BYTE	0	G区
0006	Varbyte2		BYTE	0	G区
0007	Varbyte3		BYTE	0	G区
0008	Varbyte4		BYTE	0	G区
0009	Varbyte5		BYTE	0	G区
0010	Varbyte6		BYTE	0	G区

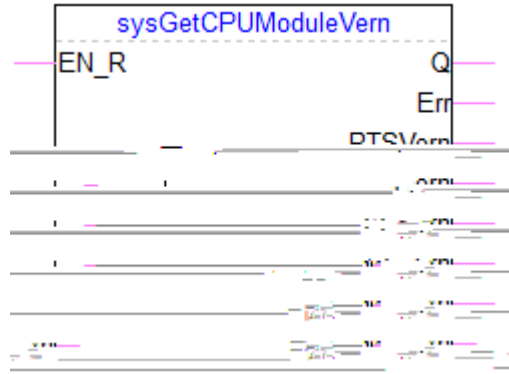


IN      DateTime    DT                    PLC                    Year    WORD  
 Month Date    Hour    Minute    Second    Week            BYTE  
           Q      TRUE  
 EN      Q      FALSE    DateTime    Year    Month    Date    Hour    Minute    Second    Week

## 2.4.2

### 2.4.2.1 sysGetCPUModuleVern

1.



133 sysGetCPUModuleVern

2.

EN_R	BOOL	FALSE TRUE
------	------	---------------

Q	BOOL	TRUE FALSE	FALSE
Err	BYTE	128	0
RTSVern	DWORD	RTS 16	build 1 RTS bulid 16 0 RTS bulid

OSVern      DWORD

序号	变量名	变量说明	变量类型	初始值	区域
MODULEVERN_1					
SYSGETCPUMODULEVERN					
			G区	0001	SYSGETCPU
		BOOL	FALSE	G区	0002 Varbool1
		BOOL	FALSE	G区	0003 Varbool2
					0004
					0005
					0006
	Varword2	DWORD	0	G区	0006
		WORD	0	G区	0007 Varword1
		WORD	0	G区	0008 Varword2
		WORD	0	G区	0009 Varword3
		WORD	0	G区	0010 Varword4

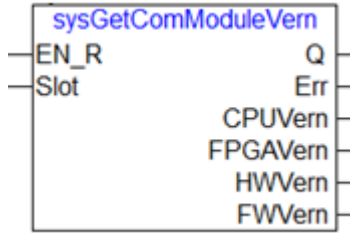
LD

FBD

### 2.4.2.2 sysGetComModuleVern

1.



**134 sysGetComModuleVern**

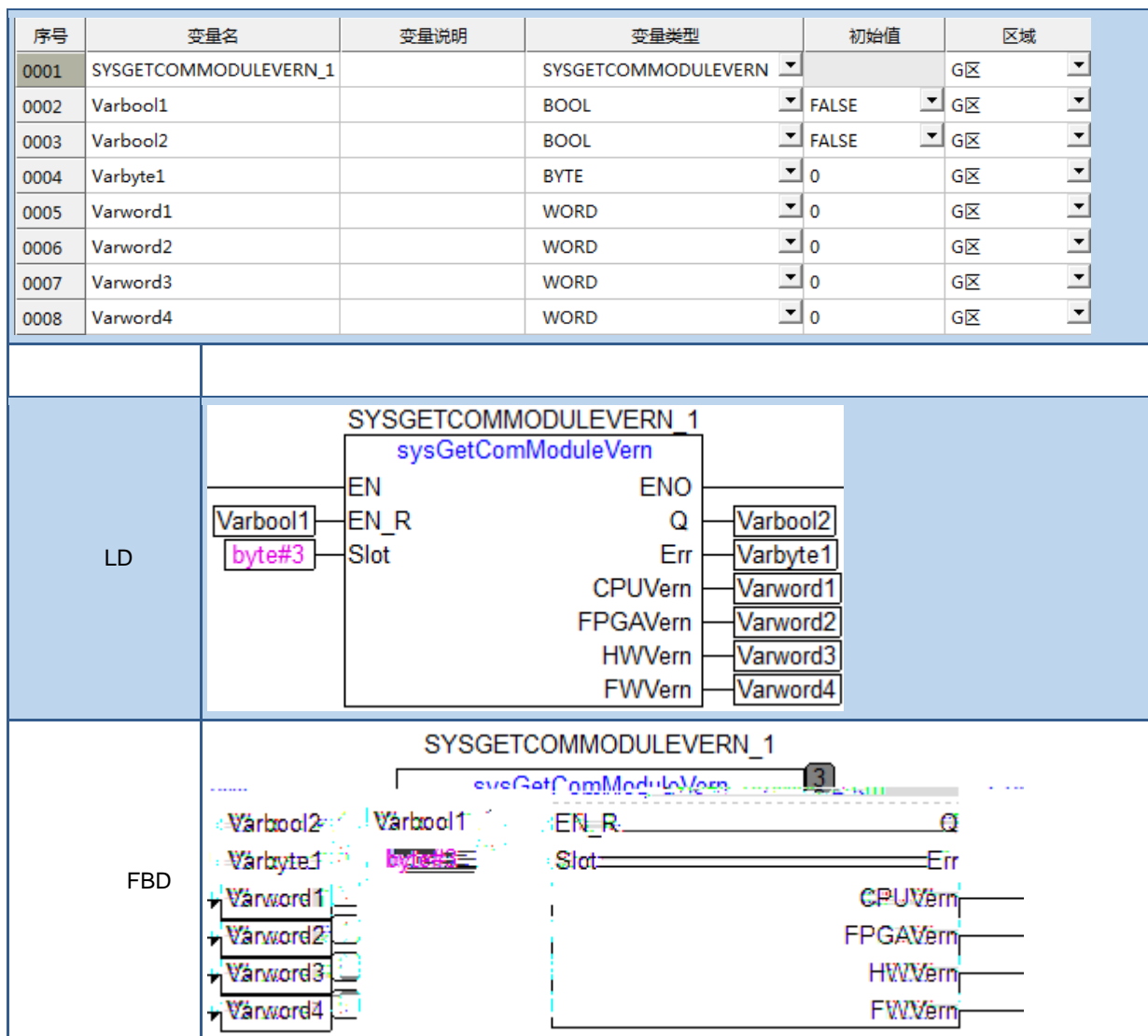
2.

EN_R	BOOL	FALSE TRUE
Slot	BYTE	2 1 2-5

Q	BOOL	TRUE FALSE	FALSE
Err	BYTE	0 1 Slot 128 129 200	0
CPUVern	WORD	CPU	build
FPGAVern	WORD	FPGA	build
HWVern	WORD	1 26	A~Z
FWVern	WORD	LK240S-A01	01

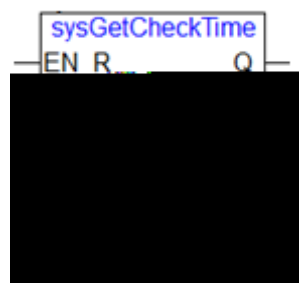
3.





### 2.4.2.3 sysGetCheckTime

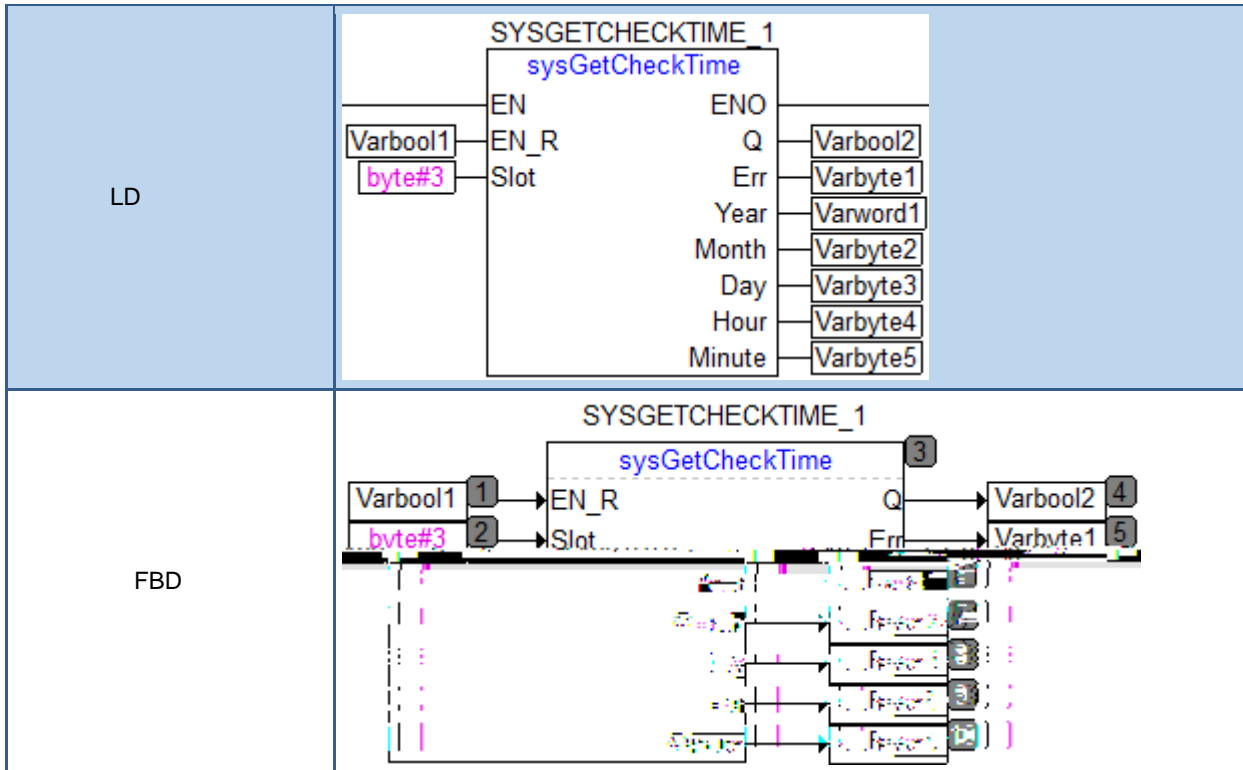
1.



135 sysGetCheckTime

2.

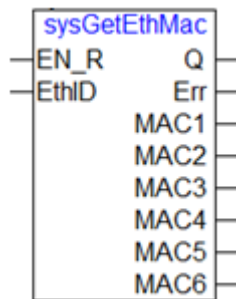




2.4.2.4 sysGetEthMac Mac

1.

Mac



136 sysGetEthMac

2.

EN_R	BOOL	FALSE		
		TRUE		
EthID	BYTE	ID	0	0-1
		0	1	
		1	2	

Q	BOOL	TRUE FALSE	FALSE
Err	BYTE	1 EthID 128 mac 200	0
MAC1	BYTE	MAC	1
MAC2	BYTE	MAC	2
MAC3	BYTE	MAC	3
MAC4	BYTE	MAC	4
MAC5	BYTE	MAC	5
MAC6	BYTE	MAC	6

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	SYSGETETHMAC_1		SYSGETETHMAC		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Varbool2		BOOL	FALSE	G区
0004	Varbyte1		BYTE	0	G区
0005	Varbyte2		BYTE	0	G区
0006	Varbyte3		BYTE	0	G区
0007	Varbyte4		BYTE	0	G区
0008	Varbyte5		BYTE	0	G区
0009	Varbyte6		BYTE	0	G区
0010	Varbyte7		BYTE	0	G区

SYSGETETHMAC 1

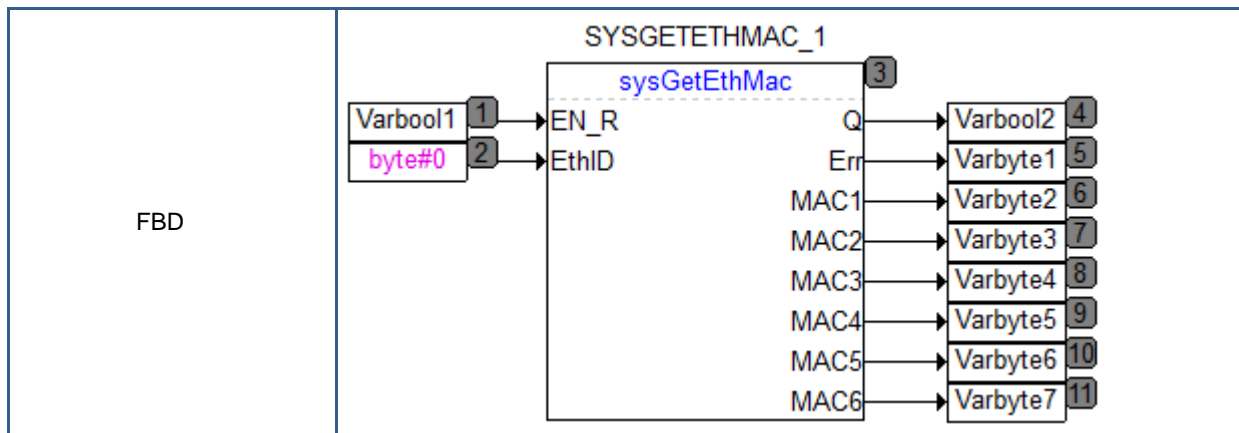
sysGetEthMac

<p>EN</p> <p>EN_R</p> <p>EthID</p>	<p>ENO</p> <p>Q</p> <p>Err</p> <p>MAC1</p> <p>MAC2</p> <p>MAC3</p> <p>MAC4</p> <p>MAC5</p> <p>MAC6</p>
------------------------------------	--

<p>Varbool1</p> <p>byte#0</p>	<p>Varbool2</p> <p>Varbyte1</p> <p>Varbyte2</p> <p>Varbyte3</p> <p>Varbyte4</p> <p>Varbyte5</p> <p>Varbyte6</p> <p>Varbyte7</p>
-------------------------------	---

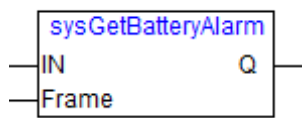
LD



1 MAC

### 2.4.2.5 sysGetBatteryAlarm

1.



137 sysGetBatteryAlarm

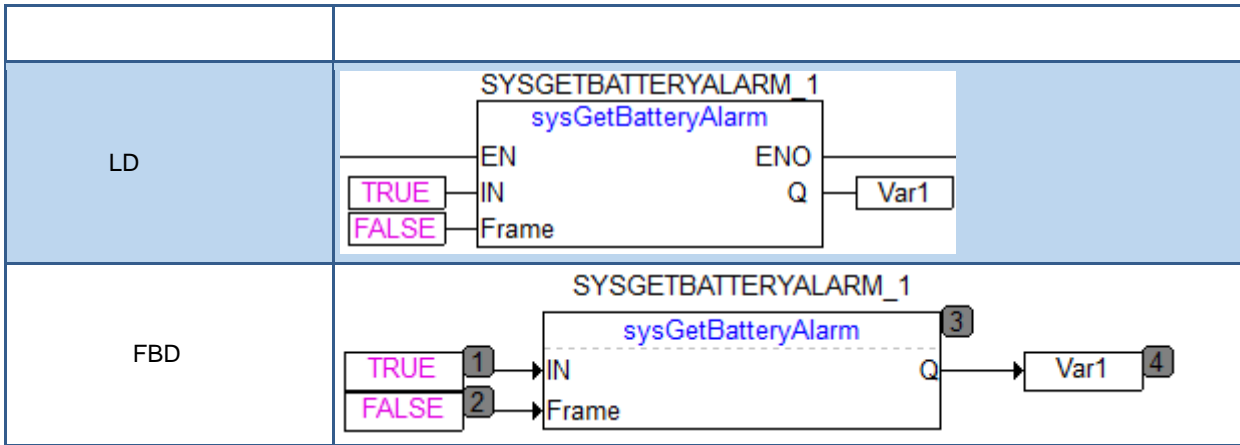
2.

Input	Type	Initial Value
IN	BOOL	FALSE
Frame	BOOL	FALSE

Output	Type	Initial Value
Q	BOOL	TURE FALSE

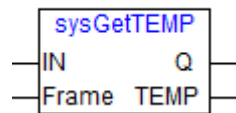
3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	SYSGETBATTERYALARM_1		SYSGETBATTERYALARM		G区
0002	Var1		BOOL	FALSE	G区



**2.4.2.6 sysGetTEMP CPU**

1.



**138 sysGetTEMP**

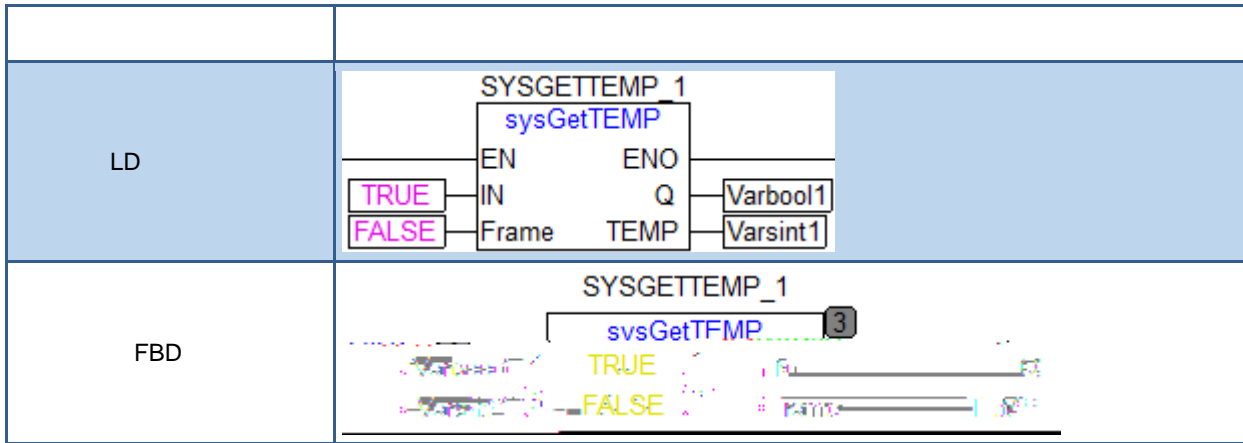
2.

IN	BOOL	FALSE
Frame	BOOL	FALSE TRUE

Q	BOOL	TRUE FALSE
TEMP	SINT	0

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	SYSGETTEMP_1		SYSGETTEMP		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Varsint1...		SINT	0	G区



### 2.4.2.7 sysGetModel

1.

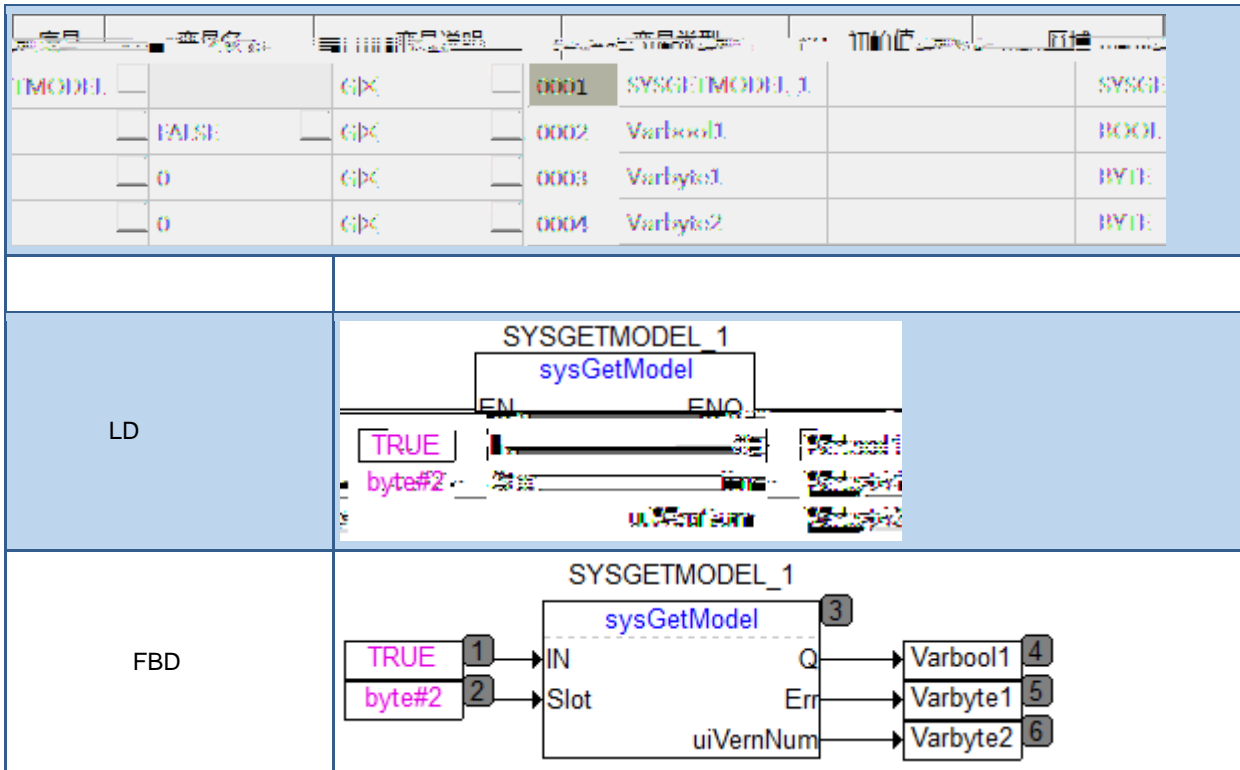


139 sysGetModel

2.

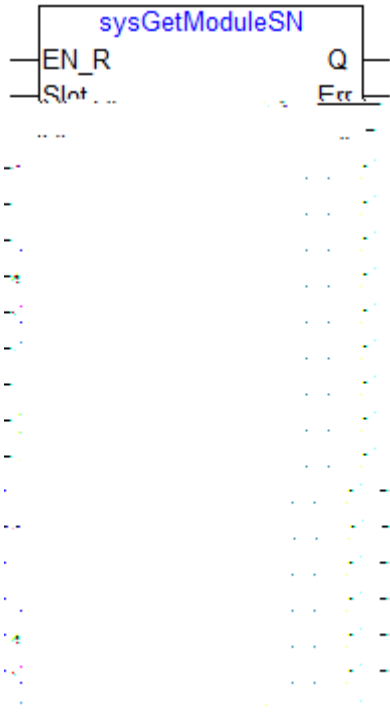
IN	BOOL	FALSE
Slot	BYTE	1 1

Q	BOOL	TRUE FALSE	FALSE
Err	BYTE	1 128	0 slot



### 2.4.2.8 sysGetModuleSN

1.



140 sysGetModuleSN

2.

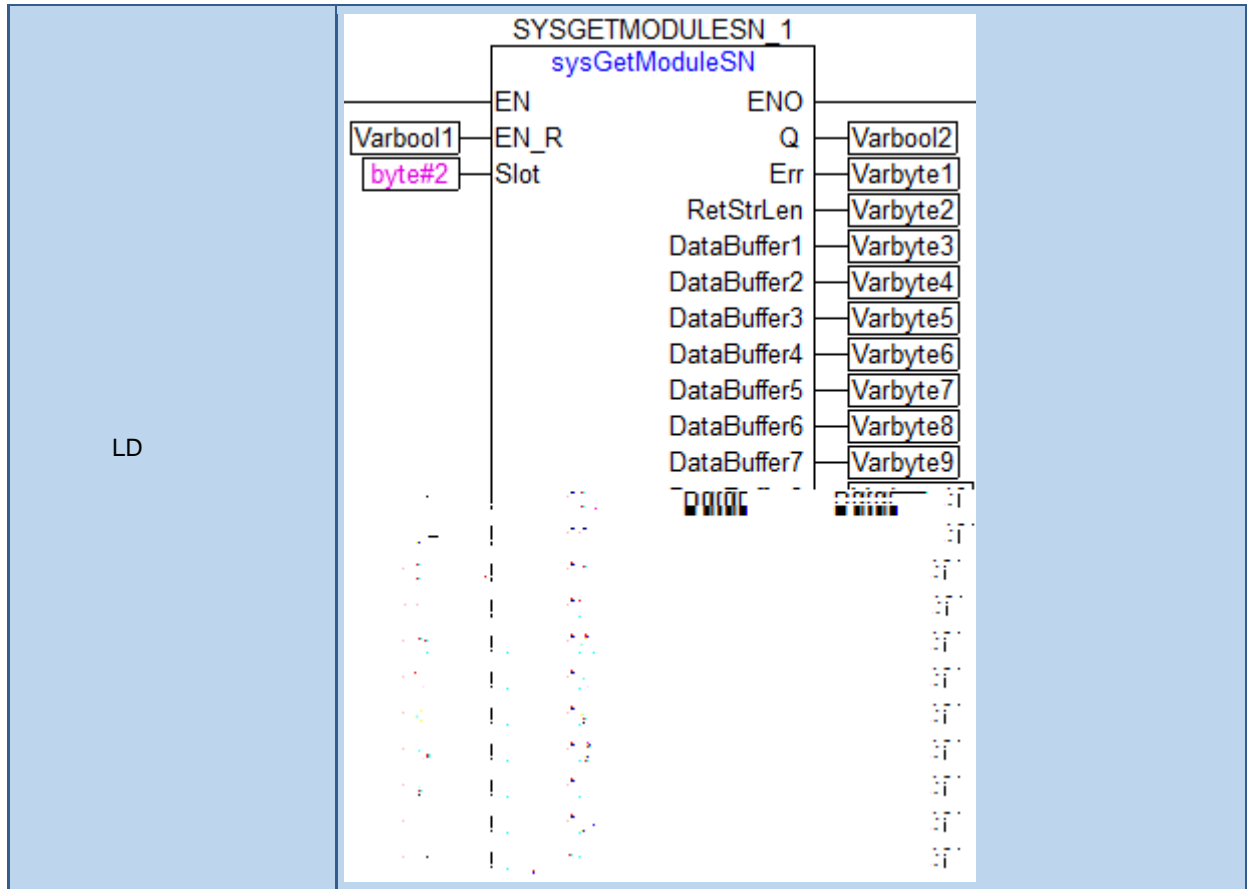
EN_R	BOOL	FALSE TRUE
Slot	BYTE	1 1-5 1

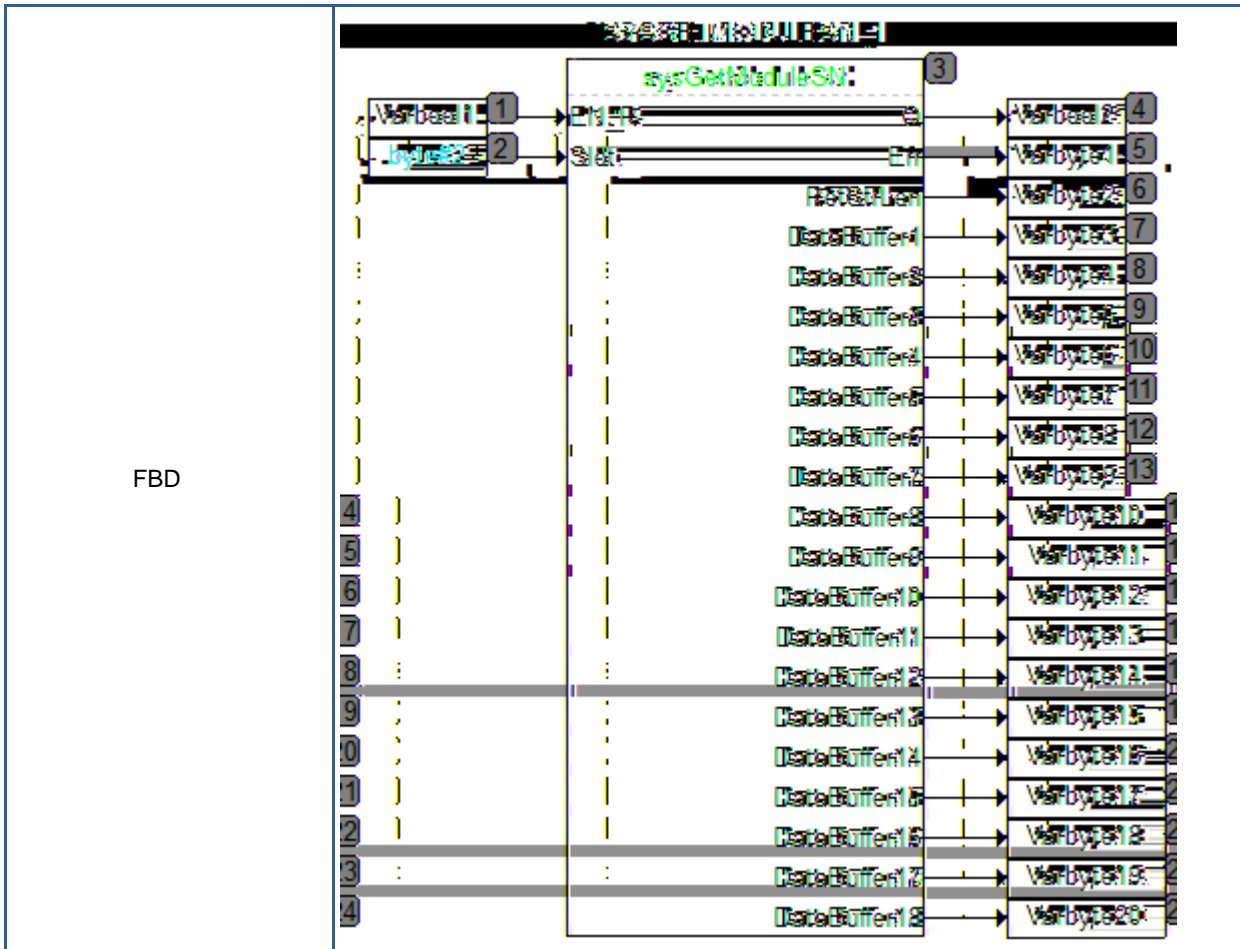
Q	BOOL	TRUE FALSE FALSE
Err	BYTE	0 1 slot 128 130 200 0
RetStrLen	BYTE	ASCII 0 18

uiDataBuffer[1]~ uiDataBuffer[18]	BYTE	ASCII	18
--------------------------------------	------	-------	----

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	SYSGETMODULESN_1		SYSGETMODULESN		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Varbool2		BOOL	FALSE	G区
0004	Varbyte1		BYTE	0	G区
0005	Varbyte2		BYTE	0	G区
0006	Varbyte3		BYTE	0	G区
0007	Varbyte4		BYTE	0	G区
0008	Varbyte5		BYTE	0	G区
					0009
					0010
					0011
					0012
					0013
					0014
					0015
					0016
					0017
					0018
					0019
					0020
					0021
					0022
					0023

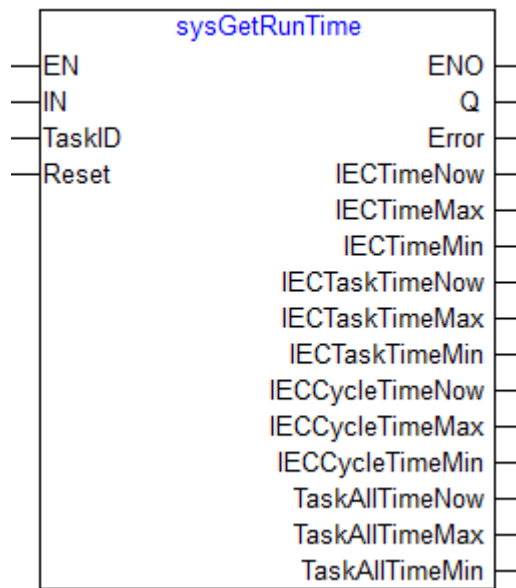




## 2.4.3

### 2.4.3.1 sysGetRunTime

1.



141 sysGetRunTime

2.

Input	Variable	Default Value
IN	BOOL	FALSE
TaskID	BYTE	IEC ID 1~8 LK220S IEC ID 1 2~8 9 0
Reset	BOOL	FALSE

Output	Variable	Default Value
Q	BOOL	TRUE FALSE
Error	BYTE	0 1 TaskID
IECTimeNow	DWORD	0
IECTimeMax	DWORD	0
IECTimeMin	DWORD	0
IECTaskTimeNow	DWORD	0
IECTaskTimeMax	DWORD	0
IECTaskTimeMin	DWORD	0
IECCycleTimeNow	DWORD	0
IECCycleTimeMax	DWORD	0
IECCycleTimeMin	DWORD	0

TaskAllTimeNow	DWORD	0
TaskAllTimeMax	DWORD	0
TaskAllTimeMin	DWORD	0

IECTime                      POU

IECTaskTime

IECCycleTime

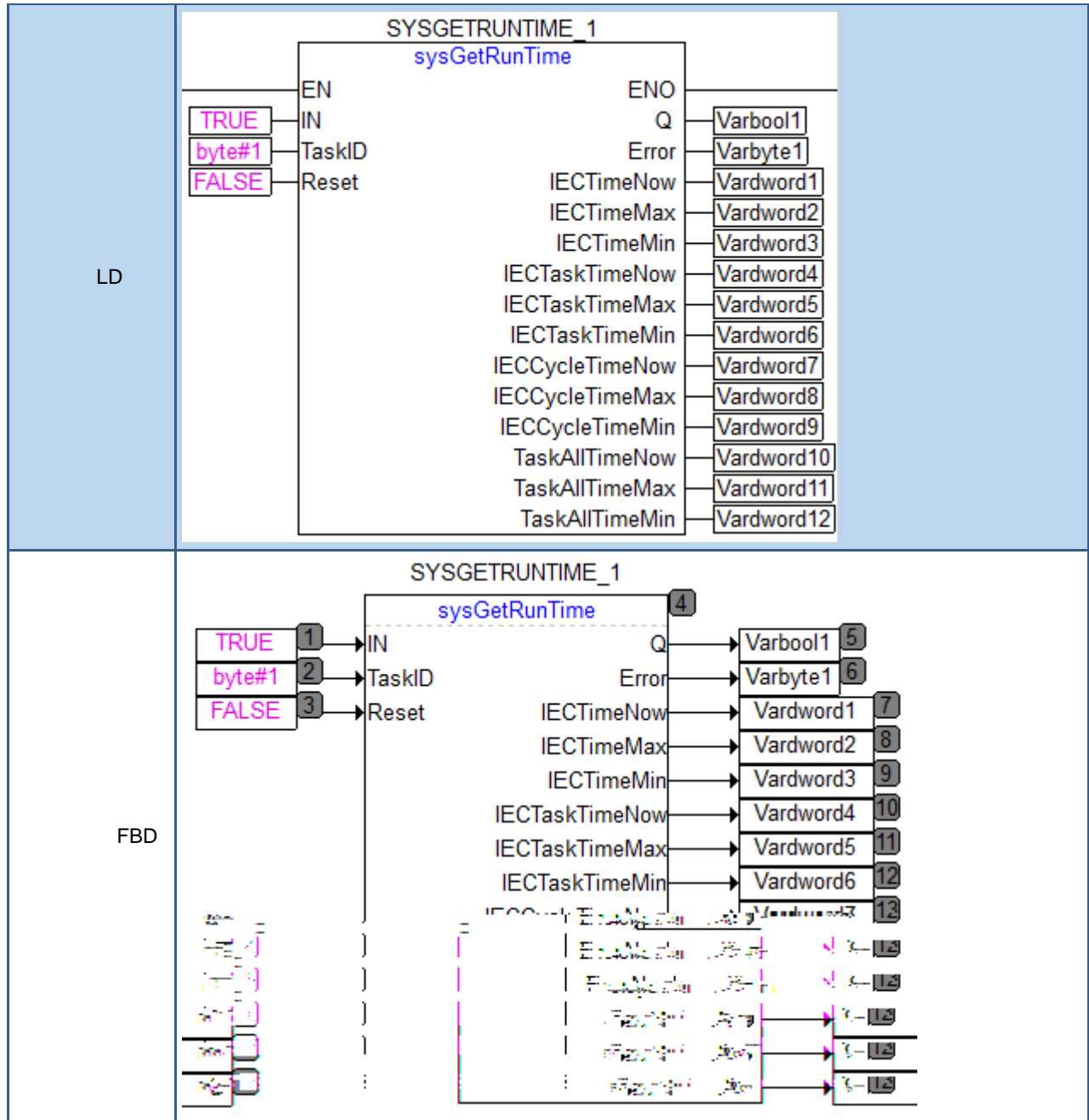
TaskAllTime



Reset	TRUE	AT	AT	AT
-------	------	----	----	----

3.

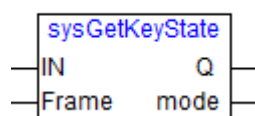
序号	变量名	变量说明	变量类型	初始值	区域
0001	SYSGETRUNTIME_1		SYSGETRUNTIME		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Varword1		DWORD		G区
0004	Varword2		DWORD		G区
0005	Varword3		DWORD		G区
0006	Varword4		DWORD		G区
0007	Varword5		DWORD		G区
0008	Varword6		DWORD		G区
0009	Varword7		DWORD		G区
0010	Varword8		DWORD		G区
0011	Varword9		DWORD		G区
0012	Varword10		DWORD		G区
0013	Varword11		DWORD		G区
0014	Varword12		DWORD		G区
0015	Varword13		DWORD		G区



### 2.4.3.2 sysGetKeyState

1.

CPU



142 sysGetKeyState

2.

IN	BOOL	FALSE
Frame	BOOL	FALSE TRUE

Q	BOOL	TRUE FALSE
Mode	BYTE	0 1 RUN 2 REM 3 PRG

3.

序号	变量名	变量说明	变量类型	初始值	区域
		0002	Varbool1		
		0003	Varbyte1		

LD	
FBD	

### 2.4.3.3 sysGetDPMasterState DP

1.

DP



序号	变量名	变量说明	变量类型	初始值	区域
0001	SYSGETDPMASERSTATE_1		SYSGETDPMASERSTATE		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Varbyte1		BYTE	0	G区
0004	Varbool2		BOOL	FALSE	G区
0005	Varbool3		BOOL	FALSE	G区
0006	Varbool4		BOOL	FALSE	G区
0007	Varbool5		BOOL	FALSE	G区
0008	Varbool6		BOOL	FALSE	G区

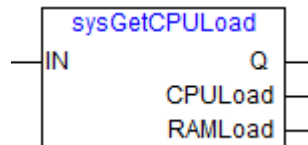
  

LD	
FBD	

### 2.4.3.4 sysGetCPULoad

1.

CPU



144 sysGetCPULoad

2.



IN	BOOL	FALSE
----	------	-------

Q	BOOL	TRUE FALSE
CPUload	REAL	CPU 0 %
RAMload	REAL	0 %

CPU DP DP / 100%

RAM /

3.

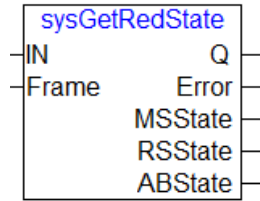
序号	变量名	变量说明	变量类型	初始值	区域
0001	SYSGETCPULOAD_1		SYSGETCPULOAD		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Varreal1		REAL	0	G区
0004	Varreal2		REAL	0	G区

LD	
FBD	

2.4.3.5 sysGetRedState

1.



145 sysGetRedState

2.

IN	BOOL	FALSE
Frame	BOOL	FALSE TRUE

Q	BOOL	TRUE FALSE	FALSE
Error	BYTE	128 11 129 130	0 0-6
MSState	BYTE	0 1 2 3 4 5 6 7 8 9 10 11	0 Ready Ready Ready
RSState	BYTE	0 1 2	0
ABState	BYTE	AB 0 1 A 2 B	0

3.



Q	BOOL	TRUE FALSE	FALSE
Error	BYTE	0 128 129 130	PRG

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	SYSMASTERSWITCHTOSLAVE_1		SYSMASTERSWITCHTOSLAVE		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Varbool2		BOOL	FALSE	G区

LD	
FBD	

### 2.4.3.7 sysGetDPSlaveState DP

1.

DP

2.

IN	BOOL	FALSE
DPAddr	BYTE	0 2-125
Slot	BYTE	DP

Q	BOOL	TRUE FALSE
---	------	---------------

		FALSE
Error	BYTE	0 1 2 128 dp
ConfigState	BOOL	TRUE FALSE
ComState	BOOL	TRUE FALSE
SlaveID	WORD	ID LKS ID 0x0500~0x05FF
DiagDataLen	BYTE	
DiagData1~81 647.9		

ETH1Err	BOOL	1	FALSE	TRUE
ETH2Err	BOOL	2	FALSE	TRUE
InterComErr	BYTE	1	0	bit
		bit0=1	2	
		bit1=1	3	
		bit2=1	4	6
		bit3=1	5	6
		bit4~bit7		

3.

序号	变量名	变量说明	变量类型	初始值	区域
0001	SYSGETCPUDIAGINFO_1		SYSGETCPUDIAGINFO		G区
0002	Varbool1		BOOL	FALSE	G区
0003	Varbool2		BOOL	FALSE	G区
0004	Varbool3		BOOL	FALSE	G区
0005	Varbyte4		BYTE	0	G区

SYSGETCPUDIAGINFO\_1  
sysGetCPUDiagInfo

LD

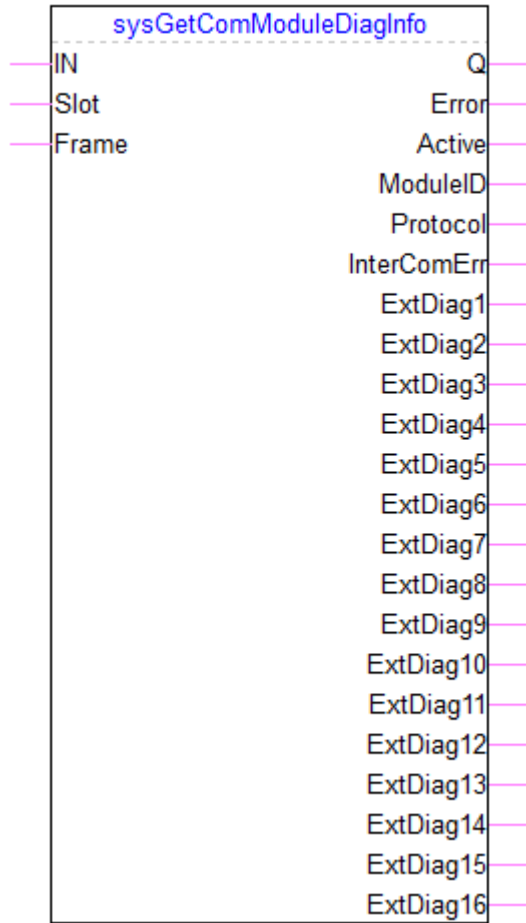
SYSGETCPUDIAGINFO\_1  
sysGetCPUDiagInfo

FBD

CPU

### 2.4.3.9 sysGetComModuleDiagInfo

1.



148 sysGetComModuleDiagInfo

2.

IN	BOOL	FALSE
Slot	BYTE	2 2-5 1
Frame	BOOL	FALSE TRUE



		0 1 Slot 128 129
Active	BOOL	FALSE TRUE 0
ModuleID	BYTE	ID 1 LK220( ) 2 LK240 3 LK249 4 LK247 5 LK248 6 LK220S ( ) 7 LK240S 8 LK249S
Protocol	BYTE	1 2 DP 3 CANOpen 4 EtherCAT 5 PowerLink 6 ModbusRTU 7
InterComErr	BOOL	0 TRUE
ExtDiag1	BYTE	LK240S 1 1 1 LK249S 1 DP1 1
ExtDiag2	BYTE	LK240S 2 2 1 LK249S 2 DP2 1
ExtDiag3	BYTE	LK240S 3 A/B 1 LK249S 3
ExtDiag4	BYTE	LK240S 4 1 LK249S 4 1
ExtDiag5	BYTE	LK240S 5 LK249S

		5		
ExtDiag6	BYTE	LK240S 6 LK249S 6	1	0-100
ExtDiag7	BYTE	LK240S 7 LK249S 7	2	0-100
ExtDiag8~16	BYTE			

**2.4.3.10 sysSetProfiOareq Profisafe**

1.

Profisafe



149 sysSetProfiOareq

2.

EN_R	BOOL	FALSE
DPAddr	BYTE	2 2-125
Slot	Byte	DP

Q	BOOL	TRUE FALSE	FALSE
Error	BYTE	0 1 2 3	0 Profisafe





**和利时集团**  
**HollySys Group**